



# **Sonoma Valley Unified School District**

## **Technology Review**

*March 26, 2009*





**CSIS California School Information Services**

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March 26, 2009

Pamela Martens, Ph.D., Superintendent  
Sonoma Valley Unified School District  
17850 Railroad Avenue  
Sonoma, CA 95476

Dear Superintendent Martens:

In August 2008, the Fiscal Crisis and Management Assistance Team (FCMAT) entered into an agreement with the Sonoma Valley Unified School District for a review of the district's technology operations and services. Specifically, the agreement asked FCMAT to do the following:

1. Conduct a review of the district's administrative technology and make recommendations for improvement.
2. Conduct a review of the district's instructional technology and make recommendations for improvement.
3. Conduct a review of the district's technology services delivery and make recommendations for improvement.

The original study agreement was amended on September 25, 2009 to add the following scope point.

4. Conduct an end-to-end network discovery audit to fully document the district's network and identify network elements for replacement/upgrade.

The attached report contains the study team's findings and recommendations.

We appreciate the opportunity to serve you and we extend our thanks to all the staff of the Sonoma Valley Unified School District for their cooperation and assistance during the review.

Sincerely,

Joel D. Montero  
Chief Executive Officer

**FCMAT**

Joel D. Montero, Chief Executive Officer

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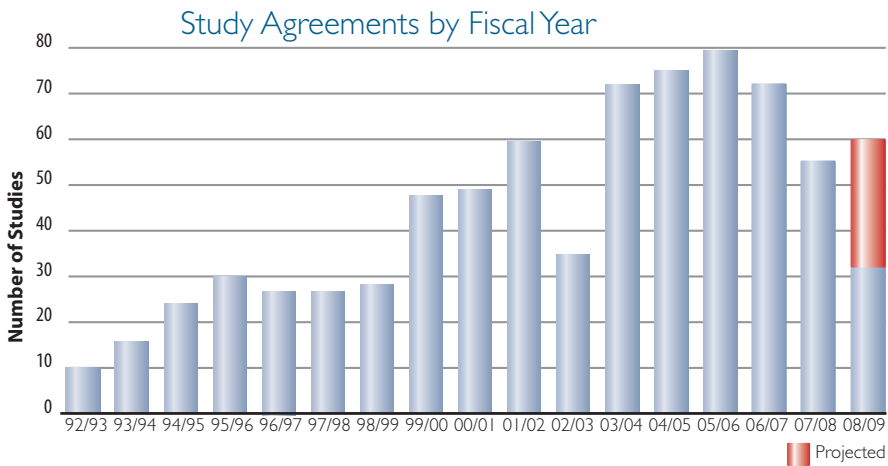
# Foreword - FCMAT Background

The Fiscal Crisis and Management Assistance Team (FCMAT) was created by legislation in accordance with Assembly Bill 1200 in 1992 as a service to assist local educational agencies (LEAs) in complying with fiscal accountability standards.

AB 1200 was established from a need to ensure that LEAs throughout California were adequately prepared to meet and sustain their financial obligations. AB 1200 is also a statewide plan for county offices of education and school districts to work together on a local level to improve fiscal procedures and accountability standards. The legislation expanded the role of the county office in monitoring school districts under certain fiscal constraints to ensure these districts could meet their financial commitments on a multiyear basis. AB 2756 provides specific responsibilities to FCMAT with regard to districts that have received emergency state loans. These include comprehensive assessments in five major operational areas and periodic reports that identify the district’s progress on the improvement plans.

In January 2006, SB 430 (charter schools) and AB 1366 (community colleges) became law and expanded FCMAT’s services to those types of LEAs.

Since 1992, FCMAT has been engaged to perform nearly 750 reviews for local educational agencies, including school districts, county offices of education, charter schools and community colleges. Services range from fiscal crisis intervention to management review and assistance. FCMAT also provides professional development training. The Kern County Superintendent of Schools is the administrative agent for FCMAT. The agency is guided under the leadership of Joel D. Montero, Chief Executive Officer, with funding derived through appropriations in the state budget and a modest fee schedule for charges to requesting agencies.



Total Number of Studies.....	743
Total Number of Districts in CA	982
● Management Assistance.....	705 (94.886%)
● Fiscal Crisis/Emergency.....	38 (5.114%)
Note: Some districts had multiple studies.	
● Districts (7) that have received emergency loans from the state. (Rev. 1/22/09)	





# Introduction

## *Background*

Located in the North Bay region of the San Francisco Bay Area, the Sonoma Valley Unified School District serves approximately 5,000 students at two high schools, two middle schools, five elementary schools, a community school, an adult school, and two charter schools. The district employs approximately 550 classified and certificated staff members and enjoys exceptional parent involvement.

In August 2008 the district and the Fiscal Crisis and Management Assistance Team (FCMAT) entered into an agreement for FCMAT to perform the following:

1. Conduct a review of the district's administrative technology and make recommendations for improvement.
2. Conduct a review of the district's instructional technology and make recommendations for improvement.
3. Conduct a review of the district's technology services delivery and make recommendations for improvement.

The original study agreement was amended on September 25, 2009 to add the following scope point.

4. Conduct an end-to-end network discovery audit to fully document the district's network and identify network elements for replacement/upgrade.

## *Study Guidelines*

FCMAT visited the district on September 4-5, 2008 to interview employees, collect data and review information. In addition, fieldwork for a network discovery audit began on November 18, 2008 and was completed on January 24, 2009. This report is the result of those activities and is divided into the following sections:

- I. Executive Summary
- II. Technology Support and Leadership
- III. Student Information System
- IV. Security
- V. Network Infrastructure
- VI. Educational Technology

### *Study Team*

The study team was composed of the following members:

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Executive Director, Technology Services  
Ventura County Office of Education  
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Bakersfield, CA

\*As members of this study team, these consultants were not representing their respective employers but were working solely as independent contractors for FCMAT.

# Executive Summary

The Sonoma Valley Unified School District can create a sustainable technology implementation program that will benefit students and teachers while performing the district's administrative functions. However, many instructional users have been unable to integrate technology into their curriculum because of antiquated equipment and the lack of a single district-level position to assist with technology planning, curriculum integration, and professional development for instructional staff. The district should consider creating an information technology (IT) director position and filling it with a visionary leader who understands technology. Responsibility for overall technology coordination should be assigned to the assistant superintendent of business services.

One staff member commented that if technology support does not improve soon, the district would be better off simply abandoning technology altogether. For many other site users, intermittent network reliability has become so frustrating that some stated that technology cannot be relied upon during instruction. A major factor contributing to this problem is that technology staff members have no tools to help monitor the status of network resources and so are unaware of network issues when they arise. In addition, there is no formal technology work order system. The district should acquire and use network monitoring tools, and implement a work order system for technology support.

No staff member is responsible for maintaining the current student information system (SIS). A lack of consistent versions, enhancements and standards for the SIS at different sites also hampers productivity, as does a lack of user training. The district should ensure that all sites are using the same version, enhancements and standards, and provide users with training. Because the district's current SIS vendor will cease its support in August 2010, the district needs to begin evaluating a new SIS. The district should create a SIS selection committee to develop a request for proposal (RFP) and guide the selection of a new SIS. The district needs to assign a project manager for a period of approximately 18-24 months to take responsibility for selecting the new SIS, converting data, and implementing the system.

The technology staging area at the high school housed a significant amount of equipment at the time of FCMAT's review, but is not secure. The district should secure this area.

The district's network infrastructure is not documented, and the network lacks a network management system. In addition, many servers are not capable of meeting network demands. The district's wireless network services are also lacking in reliability and security. The district should document its network, implement network management, and ensure that all servers are server-grade computers from major manufacturers. The district should also ensure that all wireless access points support the latest security standards.

The district uses microwave radio links for its core network infrastructure, but the geographic topography of the area may make this technology unreliable. The district should evaluate this technology and consider replacing it with fiber-optic services from a local provider if warranted.

Although the district's teachers and administrators are eager to integrate technology into classrooms, labs and libraries, technology department staff members expressed a sense of disconnect from instructional staff and those responsible for curriculum. As a result, site needs and classroom technology initiatives have not been a high district priority; support for educational technology is neither consistent nor systematic. Communication is also lacking, and technology use is not tied to student achievement.

The district needs to establish a focus on educational technology so that it can be used fully and incorporated into the curricula and classrooms. The district could accomplish this by creating a new position of educational technology coordinator, or reassigning some of the curriculum and instruction coordinator's duties to free this position for more technology duties. An educational technology coordinator position would help connect the instructional technology department with the classrooms, and help provide professional development opportunities for teachers.

The district should also create a technology steering committee to establish and guide technology procedures, initiatives and standards, particularly for educational technology and curriculum integration.

## Findings and Recommendations

### *Technology Support and Leadership*

Technology department staff members have found it increasingly difficult to keep pace with the rapidly increasing demand for support. A number of factors make it impossible to determine the level of technology staffing the district needs. However, there is a notable deficiency in the area of technology leadership. Sites do not have technology committees to provide district administrators with feedback, information and assistance determining technology needs and funding. Numerous site staff indicated that the technology infrastructure is inadequate and unreliable, yet the program manager of management information services appeared to be unaware of these problems. The district's governing board eliminated the position of program manager of management information services effective January 13, 2009.

The district needs to consider creating an IT director position and filling it with a visionary leader who understands technical equipment and processes. Interviews made it clear that no existing staff member possesses the skills needed to fill this role. Key skills and responsibilities of this position would include not only technical knowledge, but also the ability to provide leadership for the IT staff and maintain excellent communication with all staff and administrators, particularly those in the curriculum and instruction department. Once the individual selected for this position becomes familiar with the current environment, they should be able to determine appropriate staffing levels for technology.

The information technology (IT) department lacks the communication channels and leadership skills needed to successfully guide the district in integrating technology into the classroom. There is a lack of sensitivity to curricular needs and a lack of communication with instructional staff. An administrator is needed to successfully integrate technology support services with the district's instructional goals and initiatives.

Interviews revealed a lack of customer service orientation among technology support providers, and the district lacks a formal work order system for technology support. Because of this, FCMAT was not able to determine the extent to which the IT department tracks support needs or ensures timely support. Because site staff members' e-mail requests for support often go unanswered, they sometimes copy district administrators on requests to increase the likelihood of response. This has resulted in inefficient use of administrative and IT support staff time.

Numerous robust and comprehensive technology work order systems are available. A system such as this can serve as an excellent tool for scheduling support and is needed to enable users to track the progress of service requests.

Because there is no formal hierarchy of responsibility for technology support, users often do not know whom to contact for a particular issue. Users need to know whom to call for help when problems occur. This is especially true for student information system (SIS) support issues. Creating and distributing a simple list of individuals to contact for various support needs can increase users' trust and confidence.

Hardware and software is installed informally and with little advance notice from IT staff. For example, a recent donation of 280 Macintosh PowerBook laptop computers provided one computer for each teacher, but distribution and installation was poorly planned and left many teachers frustrated.

Wired network jacks in classrooms are connected to the network only as needed, and the district's local wireless infrastructure is not sufficiently consistent or reliable to ensure confidence among staff or to provide access for many teachers with laptops. Thus many laptop users are disconnected from the network. Providing a greater number of connected wired network ports would allow teachers to use their laptops.

Technology support staff are assigned to sites on an informal rotating schedule. Unfortunately, because emergencies often take support staff away from scheduled visits, issues can remain unresolved for months. New equipment and software sometimes sits unboxed for a long time because of inadequate strategies for distribution and a lack of staff time for installation.

Interviews revealed that all IT staff members support including Apple Macintosh computer products; however, staff lack formal training in Macintosh support and repair. Two computer platforms are more difficult to maintain than one, but give users more flexibility and an increased ability to acquire and use appropriate tools. A supportive IT staff always increases the chance of success when two computer platforms are present.

### **Recommendations**

*The district should:*

1. Consider creating a position of IT director and filling it with a visionary leader who understands technology and is able to act at the IT leader for the district. The IT director should report to the assistant superintendent of business services.
2. Establish a Teacher on Special Assignment (TOSA) position to improve the focus on educational technology and help provide teachers with technology-related professional development. The TOSA should report to the director of curriculum and instruction.
3. Give the assistant superintendent of business services responsibility for districtwide coordination of technology services. The assistant superintendent should work with the TOSA, site administrators and other IT support staff to develop a common understanding of how technology might be used to improve

instruction, increase technology training, and align technology service delivery with the district's goals and initiatives. In addition, the TOSA should work closely with the assistant superintendent of business services to ensure that the delivery of IT support services is aligned with the district's instructional initiatives.

4. Implement a formal technology work order system as soon as possible, and train site staff to help users report and track problems.
5. Ensure that sites are provided with list of whom to contact for various technology support issues.
6. Ensure that future technology distributions and installations are systematic and include communication with users and pilot programs to determine the most effective methods and strategies.
7. Consider forming site-based technology committees to provide district administrators with feedback and information to help them determine technology needs and funding.
8. Create, follow and provide to sites a formal schedule for technology support at sites. Remove support staff from scheduled support tasks only in severe emergencies.
9. Provide IT support staff with formal training in the repair and support of Macintosh computer hardware and software.





## *Student Information System*

The district has used the School Administration Student Information System (SASIXp) for many years. However, the company that markets SASIXp has announced that support for the product will not be available after August 2010. Without vendor support, the district will be unable to resolve system issues. The district needs to begin evaluating other student information systems (SISs) immediately and select a new SIS before August 2010.

No staff member has been assigned responsibility for student information system maintenance, data accuracy or integrity, and the program manager of management information services is often too busy to respond to SIS user needs, perform software updates or assist with user support issues. Because the SIS is essential to district operations, maintenance roles and responsibilities need to be assigned.

The district needs to assign a project manager for a period of approximately 18-24 months to take responsibility for selecting the new SIS, converting data, and implementing the system. The project manager would document all selection and conversion processes and work procedures. The ideal individual would have experience with project management, data migration and California School Information Services (CSIS) data submission.

An SIS selection committee could help identify and evaluate SIS alternatives, and develop a request for proposal (RFP) for a new system. The Alameda County Office of Education (county office) may also be able to assist with developing a procedure for the RFP. RFP development would need to include a detailed evaluation of the current SIS's strengths and weaknesses, as well as a list of desirable features for a new SIS. The committee would need to meet weekly, provide written agendas in advance, and maintain written minutes for approval at the following meeting.

Groups of employees with the same or similar job duties would also need to evaluate potential new SIS systems. Vendor presentations focused on the needs of specific groups of employees with similar jobs would help ensure a thorough evaluation.

Some SIS software enhancements developed by technology department staff have not been deployed at all school sites. Staff at sites that have the enhancements stated that they have numerous typographical errors that have not been corrected. Although these do not affect the functioning of the SIS, their continued presence harms the technology department's image and credibility. In addition, there are different versions of the SIS software at various school sites, making it more difficult to retrieve and use student data for reporting.

SIS users expressed a desire for more training. Several site staff members also indicated that a lack of district standards has resulted in different codes being used at different sites. As a result, SIS data often inconsistent and inaccurate, which means that it must be corrected before it is exported for state reporting, EduSoft and other student assessment purposes. The district contracts with a private vendor to make these corrections.

### *Recommendations*

#### *The district should:*

1. Consider appointing a project manager for 18-24 months to take responsibility for all tasks associated with SIS selection and conversion.
2. Establish an SIS selection committee to identify and evaluate alternatives to the current system. The committee should be comprised of representatives from classified, certificated, and administrative employee groups. Because of the central importance of an SIS, the committee should be co-chaired by the project manager and the coordinator of curriculum and instruction.
3. Assign responsibility for SIS maintenance and support to the coordinator of curriculum and instruction. The coordinator should work with the SIS selection committee to develop standards for SIS use to ensure data integrity and uniformity throughout the district.
4. Give the SIS selection committee the responsibility of creating, by the end of 2009, a detailed request for proposals (RFP) document for procuring a new SIS.

The SIS selection committee should also create the following:

- A scoring rubric to evaluate vendor responses to the RFP
  - A projection of the five-year costs of the top three proposals for a new SIS. The projection should include costs for the following:
    - hardware/storage
    - desktop upgrades and deployment
    - data migration
    - integration with existing related systems such as food services, transportation, and special education
    - training and documentation
5. Ensure that groups of employees with the same or similar job duties (e.g., registrars, attendance clerks, counselors, site administrators) are able to attend SIS vendor presentations to evaluate system functionality firsthand. Each job-alike group should attend presentations focused on their unique requirements for use of the system. Each vendor presentation should be one half to one full day long. Individual members of job-alike groups should take detailed notes and prepare informal rankings of each potential SIS for later discussion.
  6. Make the SIS selection committee responsible for forwarding information about the selected SIS to the board no later than May 1, 2010 for approval.
  7. Require technology staff to immediately correct all typographical errors in the SIS software enhancements.

8. Take action to ensure that all sites throughout the district are using the same SIS version and software enhancements.
9. Consider increasing the coordinator of curriculum and instruction's position from 0.8 FTE to 1.0 FTE and assigning this individual the responsibility of developing workshops and training for SASIxp SIS users.



## Security

A large room at the high school is used as a technology staging area. The room has a large uncovered window and contained a great deal of computer equipment at the time of FCMAT's fieldwork, including computers with the Windows operating system, Apple Macintosh laptops, and printers. Although FCMAT did not perform an inventory, the study team estimates that the total value of the equipment exceeds \$100,000.

If the district continues to use this room, it would be wise to place an opaque covering over the window and install a keyless entry system on both the main door and the store-room door. This would ensure that each door remains locked but would allow IT staff members to enter and exit as needed.

Technology support staff and school site technology coaches do not have the permissions they need to access equipment at school sites.

## Recommendations

*The district should:*

1. Place an opaque covering over the window to the technology staging area room at the high school, and install a keyless entry system at this location.
2. Provide technology support staff and school site technology coaches with the permissions they need to access equipment at their sites.



## *Network Infrastructure*

The district's network infrastructure is not documented. Accurate network documentation is critical to the successful management of any production network. FCMAT's network discovery audit of the district's network is contained in Appendix C. Network documentation needs to be reviewed for accuracy and updated at least once per quarter. The district network does not include a network management system. A network management system allows for continuous automated measurement of the network's performance, and automated collection and notification of critical network events.

To realize the maximum benefit from a network management system, all network elements in the district network should support the Simple Network Management Protocol (SNMP). Network elements that do not support SNMP should be scheduled for replacement. Adequate SNMP software and hardware platforms can be found via the Internet. Regardless of which hardware and software is purchased, installing a modem in the SNMP server would allow it to transmit notices of important network events to the technology support staff via text messages. A system such as this would need to be installed by a staff member or consultant who has experience with network management systems.

The district's core network infrastructure may be unsuitable because of the topography of the district's geographical area. The district's use of unlicensed microwave radio links may be problematic because the rolling hills and high density of trees present significant challenges to the successful operation of a microwave-based network. The district needs to have a qualified contractor review the microwave infrastructure to ensure optimum performance. The district also needs to investigate the cost of replacing the microwave network with fiber-optic services from the local exchange carrier or cable television company.

Most of the district's servers were built using clone desktop computers and are not able to meet network demands. Network servers need to be server-grade computers purchased from a major vendor such as Dell, HP or Apple.

Several staff members shared complaints regarding the lack of performance and unreliability of wireless network services. The current wireless infrastructure uses the Wireless Encryption Protocol (WEP) to provide security. This level of encryption is not secure, and detailed instructions on how to circumvent WEP encryption are widely available on the Internet. A Google query for "crack wep 60 seconds" returns more than 20,000 hits. The current best practice standard for wireless local area network (LAN) security is 802.11i. This includes the use of Wi-Fi Protected Access 2 (WPA2), Advanced Encryption Standard (AES) encryption, and 802.1x-based authentication. All of the district's wireless access points need to support the 802.11i standard; any that do not support this standard need to be replaced.

Because the district does not maintain spare network hardware on site, network resources could be interrupted for several days in the event of a network device failure. The district needs to keep a small amount of spare network hardware on hand to enable immediate replacement of failed hardware. For network elements that are too expensive to keep spares on hand, a vendor support contract that provides replacement within one business day can be useful.

Some site staff members stated that some classrooms sustained flood damage in recent years, which affected the network cabling. This is horizontal copper cabling, and proper repair or recertification would need to be performed by a qualified contractor using a full Electronic Industries Alliance (EIA) 568-B electrical test to the Category 5E standard. There was no indication that the damaged cabling had been repaired or recertified.

### **Recommendations**

*The district should:*

1. Ensure that the newly created network documentation is reviewed quarterly and updated as required to maintain accuracy.
2. Purchase and install a network management system capable of providing SNMP-based network administration.
3. Ensure that all network elements support SNMP-based management. Any network elements that do not support SNMP-based management should be scheduled for replacement.
4. Ensure that the SNMP server has a modem installed that enables text messaging of technology support staff regarding important network events.
5. Have a qualified contractor review the current microwave infrastructure to ensure optimum performance. If the network is retained, ensure that a qualified contractor reviews it annually for proper operation and alignment.
6. Consider replacing the microwave network with fiber-optic services from the local exchange carrier or local cable television company.
7. Ensure that all network servers are server-grade computers purchased from a major vendor such as Dell, HP or Apple. Servers should be rack-mountable and should be connected to uninterruptible power supplies. Disk-based RAID (random array of independent disks) and dual power supplies in each server will also improve server performance and availability. Each server's operating system should also be configured to support SNMP so that the server can be included in the network management system.
8. Ensure that all wireless access points support the 802.11i standard as soon as possible, and replace any that do not support this standard.



9. Keep on hand a small amount of network element spares to enable the immediate replacement of a failed element. For costly network elements, establish a vendor support contract that guarantees delivery of a replacement within one business day.
10. Ensure that a qualified contractor repairs or recertifies any flood-damaged horizontal copper cabling. Replace or designate as unusable any cabling that does not pass the test.



## *Educational Technology*

The district has an enthusiastic and energetic staff of teachers and administrators who are eager to embrace the integration of technology into classrooms, labs and libraries. The district also has a community of donors who give generously to help provide technology solutions. However, teachers and administrators expressed frustration with technology staff members' lack of involvement in supporting instructional technology initiatives, addressing instructional technology issues, and resolving various problems with instructional technology applications. No one in the district is responsible for working with the chief business official, the director of curriculum and instruction, and site principals to establish a technology budget that takes advantage of all available funding opportunities.

### *Technology Department Involvement*

There are some isolated instances of technology integration with the curriculum, but they are due almost solely to the efforts of individual teachers, with little or no plan for consistent maintenance or support from the technology department.

For example, a community member recently donated 250 Apple Macintosh laptop computers, one for every teacher in the district, to increase communication among teachers and improve access to Internet resources for curriculum integration. Although it was clear that the teachers would benefit from the laptops, there was little or no involvement from the technology support staff regarding installation and distribution, building of infrastructure, or planning for maintenance to ensure success.

As a result, most teachers expressed frustration regarding the lack of connectivity, access to LCD projectors for classroom presentations, ability to print to network printers, and ability to synchronize files on site servers. Because of this, only half of the donor's original hopes for the laptops have been met. One staff member commented that although teachers appreciate the laptops, they have provided virtually no instructional benefits. Without clear direction and an established plan for implementation, even donations that appear attractive can be mismanaged and have a detrimental effect.

Another example of inadequate technology department involvement occurred when some instructional staff members recently attempted to increase access to computer technology while minimizing costs by installing Xtenda lab computers. This technology extends computer access by sharing each system among multiple students. However, these efforts have been hampered by a lack of pilot testing and project management and thus have not been as reliable or effective as anticipated.

Technology support staff recently installed a new Internet content filter. Several instructional staff members expressed concerns regarding profiles developed for content filtering and the lack of collaboration, communication or testing of these profiles prior to implementation. There is no clear method for communicating with technology support staff members regarding how content profiles should be created and/or modified.

A technology-enriched curriculum will only be effective when there is systemic and institutional belief in and support of it, as well as a continual method of measurement.

A technology steering committee (TSC) could serve as an effective means of establishing and guiding the district's technology procedures, initiatives and standards, including instructional technology, donations, and implementation of new technologies. Effective TSCs typically include staff and administrators from a variety of departments, which helps ensure interdepartmental communication and cooperation.

### *Infrastructure*

Most school sites have at least one lab available for student use. However, inequity exists between student and teacher access to technology, and there is no clear vision of the district's direction regarding how technology is to be integrated into the curriculum. There is no school plan, local educational agency plan (LEAP) or other plan that ties technology directly to student achievement. Without this direct connection, education technology will remain an ancillary component of the curriculum, with sporadic and often unsupported implementation.

### *Applications*

Because a number of the district's school sites are currently in Program Improvement, the district needs to consider adopting technology-enriched curricula that directly address student achievement needs.

District computer labs provide students with access to office applications such as Microsoft Word, PowerPoint and Excel, as well as STAR Accelerated Reader, Read 180 and a few other applications available via the Internet. Installation of these computer lab applications across the district indicates that there have been attempts at standardization, but also that they have been inconsistent, with no established method of communication between school sites and technology support staff regarding application support.

Several staff members expressed frustration regarding unreliable access to server-based applications such as Read 180 or STAR Accelerated Reader. The updating and repopulating of the student demographic databases for these applications is neither scheduled nor communicated. Several site staff members also commented that they have to reboot site-based applications and file sharing servers several times each week to keep the servers from locking up. Instructional staff expressed confusion regarding access to these servers, the passwords required to access applications, and which site and technology support staff maintain the servers. Similar concerns were shared regarding mobile laptop carts. Staff indicated that a lack of training, unreliable connectivity and a lack of procedures for using instructional applications have prevented successful mobile laptop cart use.

The district uses the EduSoft student assessment system and recently coordinated with the Sonoma County Office of Education (county office) for professional development workshops on using tools for student assessment. Several district staff members reported that the county office has provided excellent training that has helped district and teaching staff accurately assess student academic progress on state tests. However, many site and district staff shared a common concern that the student demographic data in the district's current student information system is inconsistent and often inaccurate.

### ***Professional Development***

The district's Making Education Relevant and Interactive through Technology (MERIT) program continues to be successful. This program is funded by local donations and provides stipends for 20 teachers to participate in professional development training each summer in the use of technology to enhance instruction. Clear expectations have been developed that tie technology use directly to standards-based lessons. This program has operated for a number of years; however, it is not clear how directly the district's IT staff is involved in its execution or expectations.

### ***Recommendations***

*The district should:*

1. Establish a technology steering committee (TSC) comprised of instructional technology coaches, site administrators, the chief business official, director of curriculum and instruction, superintendent, board members, and technology and facilities staff members.

TSC meetings should be held monthly and chaired by the chief technology officer (CTO). All committee members should be encouraged to submit agenda items in advance, and an agenda should be sent to all members in advance of each meeting. The TSC should focus on establishing clear goals, timelines, priorities and budgets for district technology initiatives. Committee members should work to improve communication between technology support staff and instructional technology users so that expectations, priorities and support requirements are clear to all parties.

2. Identify the areas in which student achievement most needs improvement, and develop a process for evaluating educational technology solutions that will address these needs.

The TSC should establish requirements for the proposed solutions' infrastructure, procedures and methods, and require a pilot program to gauge success prior to districtwide implementation. Proposed solutions should take into consideration both classrooms and computer labs.

3. Route all technology donations from the community through the TSC to determine if there is a need and to ensure a clear plan for implementation and support, including expected outcomes and accountability.
4. Make the TSC responsible for establishing a standard regarding which technology infrastructure should be in place in elementary, middle and high school classrooms, such as the number of data drops and electrical outlets per room; the distribution and placement of wireless access points; the number of student and teacher computers; and the number of presentation systems per classroom.
5. Make the TSC responsible for establishing a districtwide standard for computer labs, with an understanding that there may be differences between elementary, middle and high school labs.

The TSC should determine consistent hardware and infrastructure for computer labs, as well as the software to be used and supported. Standards-based, grade-appropriate software should be selected that address students' academic needs. Peripherals such as display devices, printers, scanners, document cameras and interactive white boards should also be standardized to allow IT staff to support these labs.

Site principals should work with staff to determine and clearly communicate lab use schedules. Schedules should be posted and monitored to ensure that teachers are using the labs effectively. Teachers should run reports regularly to ensure that students are making adequate academic gains.

The labs should be used for differentiated instruction. The labs can also be used to give students the opportunity to use productivity tools such as word processing or PowerPoint presentations. These tools should be used for direct curriculum integration based on the International Society for Technology Integration's (ISTE's) National Educational Technology Standards (NETS). More information on these standards can be found on the following page of the ISTE Web site:

[http://www.iste.org/Content/NavigationMenu/NETS/ForStudents/2007Standards/NETS\\_for\\_Students\\_2007.htm](http://www.iste.org/Content/NavigationMenu/NETS/ForStudents/2007Standards/NETS_for_Students_2007.htm)

6. Ensure that the district's information technology director works directly with the chief business official, the director of curriculum and instruction, and site principals to establish a technology budget that takes advantage of all available funding opportunities. These include E-rate, California Teleconnect Fund (CTF), K-12 Voucher monies, Enhancing Education through Technology (EETT), District Assistance and Intervention Team (DAIT), Title I, Migrant, and State instructional funding sources.
7. Ensure that the TSC, and in particular the director of curriculum and instruction, continues to seek opportunities to coordinate with the county office and the California Technology Assistance Project (CTAP) for professional development related to data assessment and curriculum applications.

8. Continue to provide the MERIT professional development program to teachers while refining the selection criteria to address areas of instructional need related to Program Improvement. Ensure that the CTO communicates directly with teachers who participate so that expectations regarding infrastructure and hardware support are met and standards-based projects are successful.





## Appendix

- A. *Sample Request for Proposal*
- B. *Network Documentation*
- C. *Network Discovery Audit Results*
- D. *Study Agreement*



## *Appendix A*

Sample Request For Proposal:  
Network Discovery Audit  
of  
Sonoma Valley Unified School District



### **Organizational Overview**

The Sonoma Valley Unified School District currently operates seven Elementary schools, two Middle schools, two High Schools, and the Sonoma Valley Adult School. The District was founded in xxxx and currently serves approximately xxxx students.

### **Project Overview**

The vendor awarded this job will perform an audit of the District’s existing computer network infrastructure. Each District site contains a Local Area Network that is connected to the District Wide Area Network. Each LAN consists of fiber optic and copper cable plants, network hardware such as Ethernet switches and wireless access points, and a variety of network servers. The vendor will document the cabling layout at each site and document where and how each piece of network hardware connects to the LAN cable plant. The vendor will also perform the same review of the District WAN. Specific deliverables are detailed below.

### **Deliverables**

1. The copper and fiber optic network cabling and network elements at each District site as well as the elements encompassing the District WAN will be documented and labeled.
  - a. Documentation will consist of a diagram of each site showing the location of the site MDF, all site IDFs, the backbone cabling that connects each IDF to the MDF, and the network elements that are connected to the site cable plant. This documentation will also include a spreadsheet for each site that lists the unique identifiers for each object detailed below along with a brief description of what the identifier references as well as the physical location of the object the identifier references.
  - b. All Racks and Cabinets, Patch Panels, Jacks, and Network elements will be labeled. Labeling will be as follows:
    - i. A unique identifier will be determined for each District site. An example might be ALTIMIRA for Altimira Middle School or SVHS for Sonoma Valley High School.
    - ii. Each MDF and IDF location at each specific site will be assigned a two digit number. The MDF will always be designated as “SITE01”, where “SITE” is the unique site identifier. Each IDF is numbered sequentially from that point.
    - iii. Each Rack or Cabinet will receive a designation of the format “SITEXXRRYY”, where “SITE” is the unique site identifier, XX is the MDF/IDF identifier, and YY is a two digit Rack/Cabinet identifier. The first rack in a MDF or IDF is designated as “RR01”; numbering increases sequentially as Racks or Cabinets are added to the MDF or IDF.
    - iv. Each Patch Panel in a Rack or Cabinet will receive a designation of the

- format “SITEXXRRYPZZ”; the previous naming conventions apply and ZZ is a two digit Patch Panel identifier. The first Patch Panel in a Rack or Cabinet is designated as “P01”; numbering increases sequentially as Patch Panels are added to the Rack or Cabinet.
- v. Each Jack located in an MDF or IDF will receive a designation of the format “JXXX”, where XXX is replaced with three digits. The first Jack on a Patch Panel is designated as “J001”; numbering increases sequentially until the last Jack in the Patch Panel has been reached.
  - vi. Each Service Jack (for example a Jack located in a classroom) will be labeled with the combination of the MDF or IDF number, the Rack or Cabinet number, the Patch Panel number, and the Jack number. For example: 03RR01P02J14 would designate IDF 03, Rack or Cabinet 01, Patch Panel 02, Jack 14.
  - vii. Each Network Element will receive a designation of the format “SITEXXRRYNEZZ”; the previous naming conventions apply and ZZ is a two digit Network Element identifier.
- c. All computers and printers connected to the site network will be located and the port they are connected to will be documented. A separate spreadsheet containing this information will be kept. This spreadsheet will contain:
- i. The jack identifier as detailed above.
  - ii. The physical location of the item, ex. Attendance Office.
  - iii. The operating system running on the computer or the model of printer.
  - iv. A brief description of the computer or printer.

All Diagrams must be submitted in both Microsoft Visio and PNG formats.

All Spreadsheets must be submitted in Microsoft Excel format.

#### Assumptions and Agreements

1. The project must be completed by <date>
2. Vendor bids may not exceed \$
3. All diagrams and spreadsheets submitted will be approved by District staff.
4. District staff will provide vendor with all necessary site access.
5. Vendor will be provided with a specific District contact to be used by the vendor during the execution of this project.

Submission Deadline

Month, Day, Year Time PM

Submit Proposal To:

Name and Address

No Faxed or Email proposals will be accepted

For Additional Information or Clarification, Contact:

Name, Address, Telephone Number

Basis for Award of Contract

Lowest Bid

Award Date

Month, Day, Year





## *Appendix B: Network Documentation*

Items included in standard network documentation include the following:

- 1) A diagram or map of each physical network site that shows how all network elements are connected to each other. Network elements are routers, switches, wireless access points or other pieces of network hardware. Each network element should be labeled on the diagram with both a hostname and a management IP address if the element supports IP based management. The interconnection points of each network element should be labeled on each individual network element. For example, if an IDF switch connects to the MDF switch via port 50, this information should be present on the diagram. The name and address of the site, the author of the diagram, and the date the diagram was last updated should also be present on the diagram.
- 2) A diagram or map of the district backbone network that interconnects all physical network sites. This diagram should contain the same information specified for the individual site diagrams. In the event that specific sites in the backbone network are not located on district owned property, the diagram should also include 24/7 site contact information for each backbone element.
- 3) A spreadsheet or database that contains specific information about each network element. This information includes: the generic type of network element (switch, router, firewall, etc...), the hostname of each network element, the management IP address of each network element, the physical location of each network element, the manufacturer of each network element, the part number of each network element, the serial number of each network element, the firmware version of each network element, the manufacturer customer support contact information for each network element, the district asset tag number for each network element, and the date of purchase for each network element.

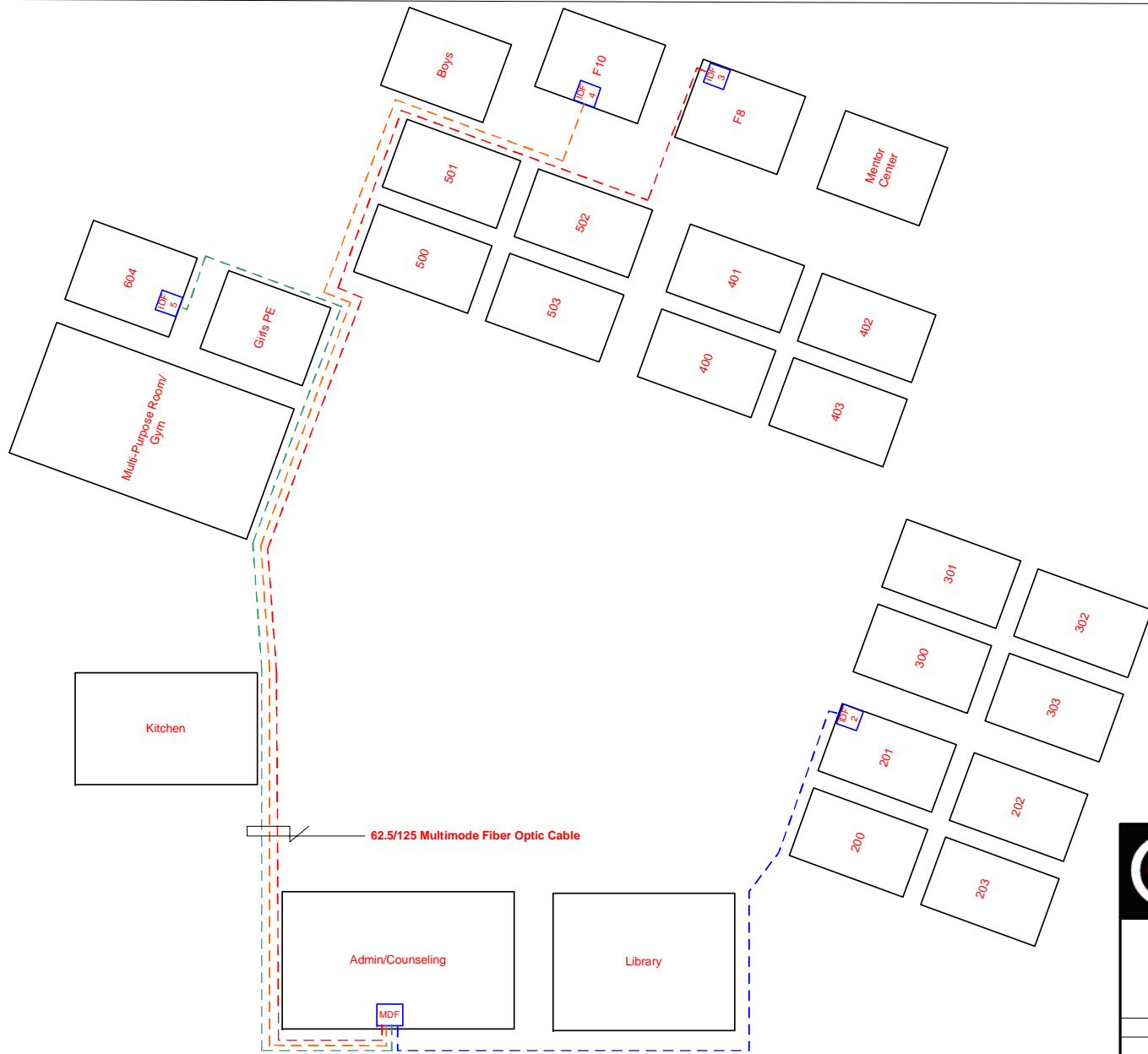
Based on the sample end-to-end network discovery audit template provided by FCMAT (see in Appendix B), and at the request of the district, a complete network discovery audit was performed. FCMAT sub-contracted with two vendors (NetBinder and Advance Data Communications), to complete the on-site portion of the audit. The FCMAT study team oversaw the audit and the work was completed in February of 2009.

The audit included physically verifying the location of and connections to every individual element in the district network. All computers and printers and network elements such as switches, routers, and wireless access points were included in the audit. Every individual horizontal cable run and fiber optic cable run at all district network sites was identified and labeled at both the MDF/IDF and jack end. The information developed by NetBinder and Advance Data Communications as a result of the audit is included as Appendix C, and should be used to create the initial draft of network documentation.



*Appendix C:*  
*Network Discovery Audit Results*






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**ADELE HARRISON MIDDLE SCHOOL**  
1150 Broadway, Sonoma, CA 95476

REVIEWED BENEDICT SIGUA	APPROVED ARNOLD DAMIAN, RCDD
STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008





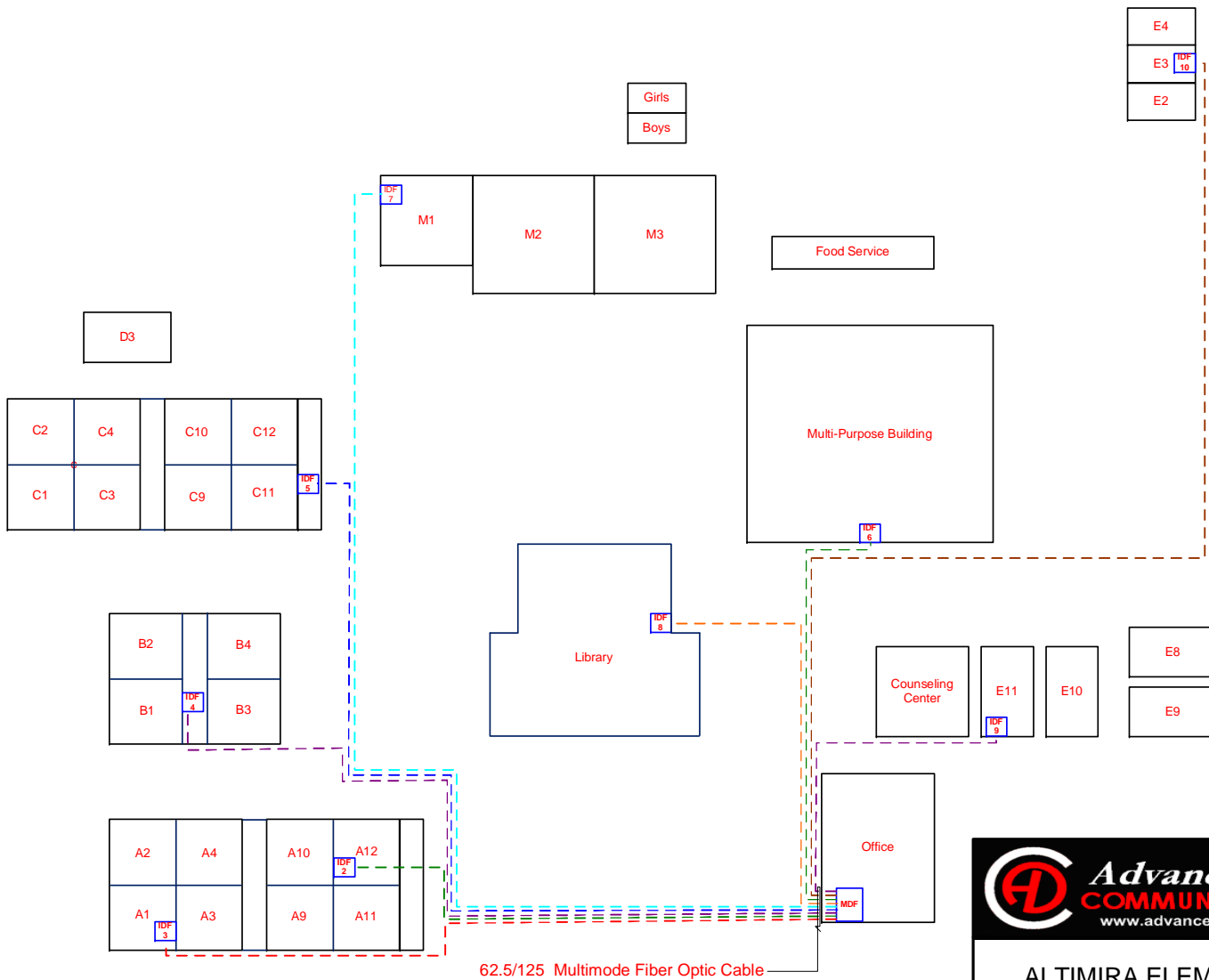












62.5/125 Multimode Fiber Optic Cable

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17805 Arnold Drive, Sonoma, CA 95476

REVIEWED BENEDICT SIGUA	APPROVED ARNOLD DAMIAN, RCDD
STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v			ALTIMIRA09RR01P01	J001		ALTIMIRA09RR01	CAN'T LOCATE
v			ALTIMIRA09RR01P01	J002		ALTIMIRA09RR01	CAN'T LOCATE
v			ALTIMIRA09RR01P01	J003		ALTIMIRA09RR01	CAN'T LOCATE
v			ALTIMIRA09RR01P01	J004		ALTIMIRA09RR01	CAN'T LOCATE
v			ALTIMIRA09RR01P01	J005		ALTIMIRA09RR01	CAN'T LOCATE
v		E11	ALTIMIRA09RR01P01	J006	09RR01P01J006	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J007	09RR01P01J007	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J008	09RR01P01J008	ALTIMIRA09RR01	NONE
v		E12	ALTIMIRA09RR01P01	J009	09RR01P01J009	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J010	09RR01P01J010	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J011	09RR01P01J011	ALTIMIRA09RR01	NONE
v		E12	ALTIMIRA09RR01P01	J012	09RR01P01J012	ALTIMIRA09RR01	NONE
v		E12	ALTIMIRA09RR01P01	J013	09RR01P01J013	ALTIMIRA09RR01	NONE
v		E12	ALTIMIRA09RR01P01	J014	09RR01P01J014	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J015	09RR01P01J015	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J016	09RR01P01J016	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J017	09RR01P01J017	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J018	09RR01P01J018	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J019	09RR01P01J019	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J020	09RR01P01J020	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J021	09RR01P01J021	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J022	09RR01P01J022	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J023	09RR01P01J023	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J024	09RR01P01J024	ALTIMIRA09RR01	NONE
v		E11	ALTIMIRA09RR01P01	J025	09RR01P02J025	ALTIMIRA09RR01	NONE
v		B1	ALTIMIRA04RR01P01	J001	01RR01P01J001	ALTIMIRA04RR01	NONE
v		B1	ALTIMIRA04RR01P01	J002	01RR01P01J002	ALTIMIRA04RR01	NONE
v		B1	ALTIMIRA04RR01P01	J003	01RR01P01J003	ALTIMIRA04RR01	NONE
v		B1	ALTIMIRA04RR01P01	J004	01RR01P01J004	ALTIMIRA04RR01	NONE
v		B2	ALTIMIRA04RR01P01	J005	01RR01P01J005	ALTIMIRA04RR01	NONE
v		B2	ALTIMIRA04RR01P01	J006	01RR01P01J006	ALTIMIRA04RR01	NONE
v		B4	ALTIMIRA04RR01P01	J007	01RR01P01J007	ALTIMIRA04RR01	NONE
v		B4	ALTIMIRA04RR01P01	J008	01RR01P01J008	ALTIMIRA04RR01	NONE
v		B4	ALTIMIRA04RR01P01	J009	01RR01P01J009	ALTIMIRA04RR01	NONE
v		B4	ALTIMIRA04RR01P01	J010	01RR01P01J010	ALTIMIRA04RR01	NONE

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v		B4	ALTIMIRA04RR01P01	J011	01RR01P01J011	ALTIMIRA04RR01	NONE
v		B4	ALTIMIRA04RR01P01	J012	01RR01P01J012	ALTIMIRA04RR01	NONE
v		B3	ALTIMIRA04RR01P01	J013	01RR01P01J013	ALTIMIRA04RR01	NONE
v		B3	ALTIMIRA04RR01P01	J014	01RR01P01J014	ALTIMIRA04RR01	NONE
v		B3	ALTIMIRA04RR01P01	J015	01RR01P01J015	ALTIMIRA04RR01	NONE
v		B3	ALTIMIRA04RR01P01	J016	01RR01P01J016	ALTIMIRA04RR01	NONE
v		C11	ALTIMIRA05RR01P01	J001	05RR01P01J001	ALTIMIRA05RR01	NONE
v		C11	ALTIMIRA05RR01P01	J002	05RR01P01J002	ALTIMIRA05RR01	NONE
v		C11	ALTIMIRA05RR01P01	J003	05RR01P01J003	ALTIMIRA05RR01	NONE
v		C11	ALTIMIRA05RR01P01	J004	05RR01P01J004	ALTIMIRA05RR01	NONE
v		C9	ALTIMIRA05RR01P01	J005	05RR01P01J005	ALTIMIRA05RR01	NONE
v		C9	ALTIMIRA05RR01P01	J006	05RR01P01J006	ALTIMIRA05RR01	NONE
v		C9	ALTIMIRA05RR01P01	J007	05RR01P01J007	ALTIMIRA05RR01	NONE
v		C9	ALTIMIRA05RR01P01	J008	05RR01P01J008	ALTIMIRA05RR01	NONE
v			ALTIMIRA05RR01P01	J009		ALTIMIRA05RR01	CAN'T LOCATE
v			ALTIMIRA05RR01P01	J010		ALTIMIRA05RR01	CAN'T LOCATE
v			ALTIMIRA05RR01P01	J011		ALTIMIRA05RR01	CAN'T LOCATE
v		C3	ALTIMIRA05RR01P01	J012	05RR01P01J012	ALTIMIRA05RR01	NONE
v		C3	ALTIMIRA05RR01P01	J013	05RR01P01J013	ALTIMIRA05RR01	NONE
v		C3	ALTIMIRA05RR01P01	J014	05RR01P01J014	ALTIMIRA05RR01	NONE
v		C3	ALTIMIRA05RR01P01	J015	05RR01P01J015	ALTIMIRA05RR01	NONE
v			ALTIMIRA05RR01P01	J016		ALTIMIRA05RR01	CAN'T LOCATE
v		C1	ALTIMIRA05RR01P01	J017	05RR01P01J017	ALTIMIRA05RR01	NONE
v		C1	ALTIMIRA05RR01P01	J018	05RR01P01J018	ALTIMIRA05RR01	NONE
v		C1	ALTIMIRA05RR01P01	J019	05RR01P01J019	ALTIMIRA05RR01	NONE
v		C1	ALTIMIRA05RR01P01	J020	05RR01P01J020	ALTIMIRA05RR01	NONE
v		C2	ALTIMIRA05RR01P01	J021	05RR01P01J021	ALTIMIRA05RR01	NONE
v		C2	ALTIMIRA05RR01P01	J022	05RR01P01J022	ALTIMIRA05RR01	NONE
v		C2	ALTIMIRA05RR01P01	J023	05RR01P01J023	ALTIMIRA05RR01	NONE
v			ALTIMIRA05RR01P01	J024		ALTIMIRA05RR01	CAN'T LOCATE
v		C4	ALTIMIRA05RR01P01	J025	05RR01P02J025	ALTIMIRA05RR01	NONE
v		C4	ALTIMIRA05RR01P01	J026	05RR01P02J026	ALTIMIRA05RR01	NONE
v		C4	ALTIMIRA05RR01P01	J027	05RR01P02J027	ALTIMIRA05RR01	NONE
v		C4	ALTIMIRA05RR01P01	J028	05RR01P02J028	ALTIMIRA05RR01	NONE
v			ALTIMIRA05RR01P01	J029		ALTIMIRA05RR01	CAN'T LOCATE

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v			ALTIMIRA05RR01P01	J030		ALTIMIRA05RR01	CAN'T LOCATE
v		C8	ALTIMIRA05RR01P01	J031	05RR01P02J031	ALTIMIRA05RR01	NONE
v		C8	ALTIMIRA05RR01P01	J032	05RR01P02J032	ALTIMIRA05RR01	NONE
v		C10	ALTIMIRA05RR01P01	J033	05RR01P01J033	ALTIMIRA05RR01	NONE
v		C10	ALTIMIRA05RR01P01	J034	05RR01P01J034	ALTIMIRA05RR01	NONE
v		C10	ALTIMIRA05RR01P01	J035	05RR01P01J035	ALTIMIRA05RR01	NONE
v		C10	ALTIMIRA05RR01P01	J036	05RR01P01J036	ALTIMIRA05RR01	NONE
v		C12	ALTIMIRA05RR01P01	J037	05RR01P01J037	ALTIMIRA05RR01	NONE
v		C12	ALTIMIRA05RR01P01	J038	05RR01P01J038	ALTIMIRA05RR01	NONE
v		C12	ALTIMIRA05RR01P01	J039	05RR01P01J039	ALTIMIRA05RR01	NONE
v		C12	ALTIMIRA05RR01P01	J040	05RR01P01J040	ALTIMIRA05RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J001	08RR01P01J001	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J002	08RR01P01J002	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J003	08RR01P01J003	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J004	08RR01P01J004	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J005	08RR01P01J005	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J006	08RR01P01J006	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J007	08RR01P01J007	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J008	08RR01P01J008	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J009	08RR01P01J009	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J010	08RR01P01J010	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J011	08RR01P01J011	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J012	08RR01P01J012	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J013	08RR01P01J013	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J014	08RR01P01J014	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J015	08RR01P01J015	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J016	08RR01P01J016	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J017	08RR01P01J017	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J018	08RR01P01J018	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J019	08RR01P01J019	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J020	08RR01P01J020	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J021	08RR01P01J021	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J022	08RR01P01J022	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J023	08RR01P01J023	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J024	08RR01P01J024	ALTIMIRA08RR01	NONE

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v		LIBRARY	ALTIMIRA08RR01P01	J025	08RR01P02J025	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J026	08RR01P02J026	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J027	08RR01P02J027	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J028	08RR01P02J028	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J029	08RR01P02J029	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J030	08RR01P02J030	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J031	08RR01P02J031	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J032	08RR01P02J032	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J033	08RR01P01J033	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J034	08RR01P01J034	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J035	08RR01P01J035	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J036	08RR01P01J036	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J037	08RR01P01J037	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J038	08RR01P01J038	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J039	08RR01P01J039	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J040	08RR01P01J040	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J041	08RR01P02J041	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J042	08RR01P02J042	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J043	08RR01P02J043	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J044	08RR01P02J044	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J045	08RR01P02J045	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J046	08RR01P01J046	ALTIMIRA08RR01	NONE
v		LIBRARY	ALTIMIRA08RR01P01	J047	08RR01P01J047	ALTIMIRA08RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J001	06RR01P01J001	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J002	06RR01P01J002	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J003	06RR01P01J003	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J004	06RR01P01J004	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J005	06RR01P01J005	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J006	06RR01P01J006	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J007	06RR01P01J007	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J008	06RR01P01J008	ALTIMIRA06RR01	NONE
v		KITCHEN	ALTIMIRA06RR01P01	J009	06RR01P01J009	ALTIMIRA06RR01	NONE
v		STAFF L.	ALTIMIRA06RR01P01	J010	06RR01P01J010	ALTIMIRA06RR01	NONE
v		STAFF L.	ALTIMIRA06RR01P01	J011	06RR01P01J011	ALTIMIRA06RR01	NONE
v		STAFF L.	ALTIMIRA06RR01P01	J012	06RR01P01J012	ALTIMIRA06RR01	NONE



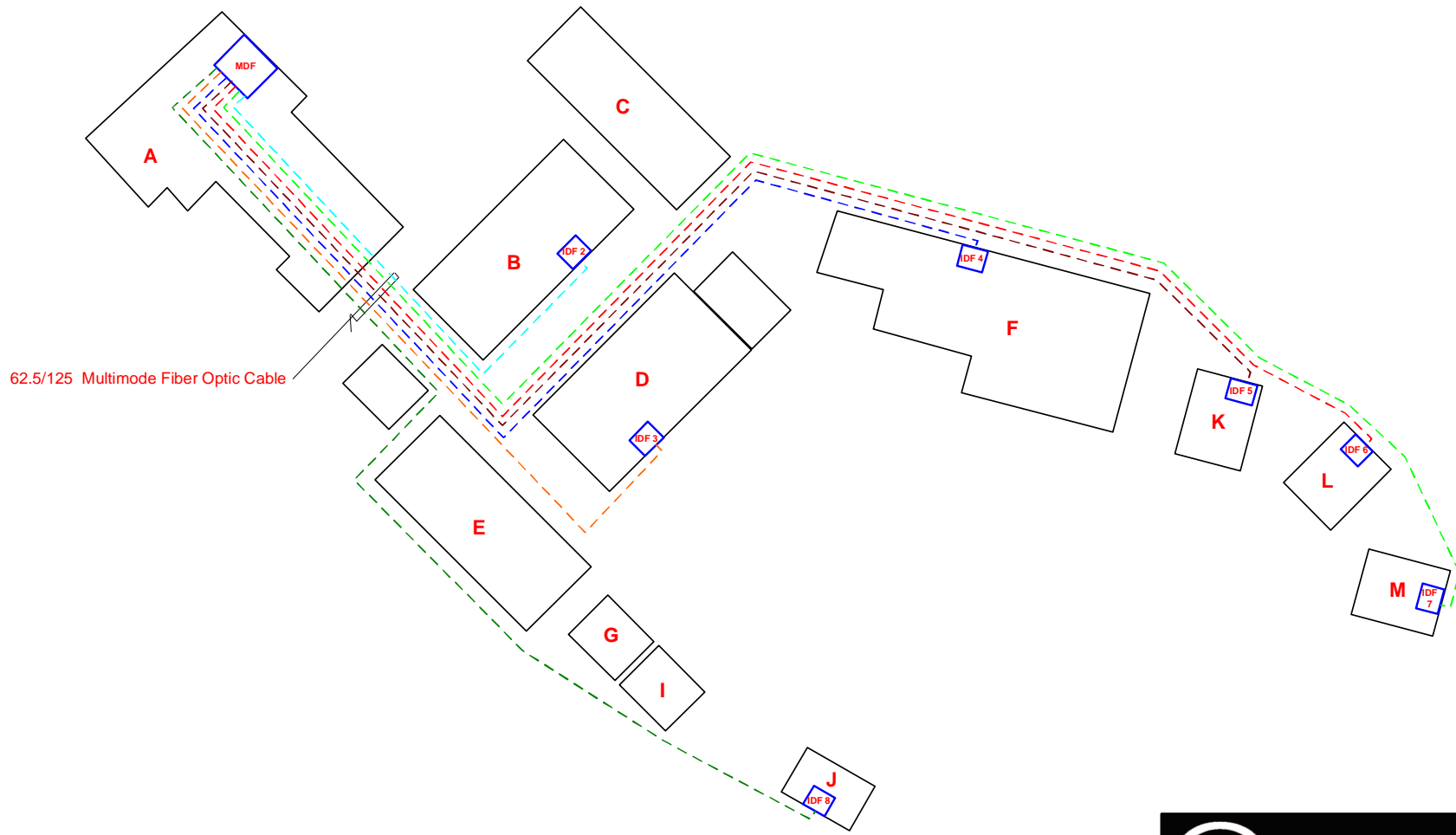
<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v		MULTI-P	ALTIMIRA06RR01P01	J013	06RR01P01J013	ALTIMIRA06RR01	NONE
v		MULTI-P	ALTIMIRA06RR01P01	J014	06RR01P01J014	ALTIMIRA06RR01	NONE
v		MULTI-P	ALTIMIRA06RR01P01	J015	06RR01P01J015	ALTIMIRA06RR01	NONE
v		MULTI-P	ALTIMIRA06RR01P01	J016	06RR01P01J016	ALTIMIRA06RR01	NONE
v		MULTI-P	ALTIMIRA06RR01P01	J017	06RR01P01J017	ALTIMIRA06RR01	NONE
v		M1	ALTIMIRA07RR01P01	J001	07RR01P01J001	ALTIMIRA07RR01	NONE
v		M1	ALTIMIRA07RR01P01	J002	07RR01P01J002	ALTIMIRA07RR01	NONE
v		M1	ALTIMIRA07RR01P01	J003	07RR01P01J003	ALTIMIRA07RR01	NONE
v		M1	ALTIMIRA07RR01P01	J004	07RR01P01J004	ALTIMIRA07RR01	NONE
v		M2	ALTIMIRA07RR01P01	J005	07RR01P01J005	ALTIMIRA07RR01	NONE
v		M2	ALTIMIRA07RR01P01	J006	07RR01P01J006	ALTIMIRA07RR01	NONE
v		M2	ALTIMIRA07RR01P01	J007	07RR01P01J007	ALTIMIRA07RR01	NONE
v		M2	ALTIMIRA07RR01P01	J008	07RR01P01J008	ALTIMIRA07RR01	NONE
v		M3	ALTIMIRA07RR01P01	J009	07RR01P01J009	ALTIMIRA07RR01	NONE
v		M3	ALTIMIRA07RR01P01	J010	07RR01P01J010	ALTIMIRA07RR01	NONE
v		M3	ALTIMIRA07RR01P01	J011	07RR01P01J011	ALTIMIRA07RR01	NONE
v		M3	ALTIMIRA07RR01P01	J012	07RR01P01J012	ALTIMIRA07RR01	NONE
v		M3	ALTIMIRA07RR01P01	J013	07RR01P01J013	ALTIMIRA07RR01	NONE
v		M3	ALTIMIRA07RR01P01	J014	07RR01P01J014	ALTIMIRA07RR01	NONE
v		A11	ALTIMIRA02RR01P01	J001	02RR01P01J001	ALTIMIRA02RR01	NONE
v		A11	ALTIMIRA02RR01P01	J002	02RR01P01J002	ALTIMIRA02RR01	NONE
v		A11	ALTIMIRA02RR01P01	J003	02RR01P01J003	ALTIMIRA02RR01	NONE
v		A11	ALTIMIRA02RR01P01	J004	02RR01P01J004	ALTIMIRA02RR01	NONE
v		A9	ALTIMIRA02RR01P01	J005	02RR01P01J005	ALTIMIRA02RR01	NONE
v		A9	ALTIMIRA02RR01P01	J006	02RR01P01J006	ALTIMIRA02RR01	NONE
v		A9	ALTIMIRA02RR01P01	J007	02RR01P01J007	ALTIMIRA02RR01	NONE
v		A9	ALTIMIRA02RR01P01	J008	02RR01P01J008	ALTIMIRA02RR01	NONE
v		A5	ALTIMIRA02RR01P01	J009	02RR01P01J009	ALTIMIRA02RR01	NONE
v		A5	ALTIMIRA02RR01P01	J010	02RR01P01J010	ALTIMIRA02RR01	NONE
v		A5	ALTIMIRA02RR01P01	J011	02RR01P01J011	ALTIMIRA02RR01	NONE
v		A5	ALTIMIRA02RR01P01	J012	02RR01P01J012	ALTIMIRA02RR01	NONE
v		A3	ALTIMIRA02RR01P01	J013	02RR01P01J013	ALTIMIRA02RR01	NONE
v		A3	ALTIMIRA02RR01P01	J014	02RR01P01J014	ALTIMIRA02RR01	NONE
v		A3	ALTIMIRA02RR01P01	J015	02RR01P01J015	ALTIMIRA02RR01	NONE

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v		A3	ALTIMIRA02RR01P01	J016	02RR01P01J016	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J017	02RR01P01J017	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J018	02RR01P01J018	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J019	02RR01P01J019	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J020	02RR01P01J020	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J021	02RR01P01J021	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J022	02RR01P01J022	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J023	02RR01P01J023	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J024	02RR01P01J024	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J025	02RR01P02J025	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J026	02RR01P02J026	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J027	02RR01P02J027	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J028	02RR01P02J028	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J029	02RR01P02J029	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J030	02RR01P02J030	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J031	02RR01P02J031	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J032	02RR01P02J032	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J033	02RR01P02J033	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J034	02RR01P02J034	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J035	02RR01P02J035	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J036	02RR01P02J036	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J037	02RR01P02J037	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J038	02RR01P02J038	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J039	02RR01P02J039	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J040	02RR01P02J040	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J041	02RR01P01J039	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J042	02RR01P01J041	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J043	02RR01P01J042	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J044	02RR01P01J043	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J045	02RR01P01J044	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J046	02RR01P01J045	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J047	02RR01P01J046	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J048	02RR01P01J047	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J049	02RR01P01J048	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA02RR01P01	J050	02RR01P01J049	ALTIMIRA02RR01	NONE
v		A2	ALTIMIRA02RR01P01	J051	02RR01P01J051	ALTIMIRA02RR01	NONE

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v		A2	ALTIMIRA02RR01P01	J052	02RR01P01J052	ALTIMIRA02RR01	NONE
v		A2	ALTIMIRA02RR01P01	J053	02RR01P01J053	ALTIMIRA02RR01	NONE
v		A2	ALTIMIRA02RR01P01	J054	02RR01P01J054	ALTIMIRA02RR01	NONE
v		A4	ALTIMIRA02RR01P01	J055	02RR01P01J055	ALTIMIRA02RR01	NONE
v		A4	ALTIMIRA02RR01P01	J056	02RR01P01J056	ALTIMIRA02RR01	NONE
v		A4	ALTIMIRA02RR01P01	J057	02RR01P01J057	ALTIMIRA02RR01	NONE
v		A8	ALTIMIRA02RR01P01	J058	02RR01P01J058	ALTIMIRA02RR01	NONE
v		A8	ALTIMIRA02RR01P01	J059	02RR01P01J059	ALTIMIRA02RR01	NONE
v		A8	ALTIMIRA02RR01P01	J060	02RR01P01J060	ALTIMIRA02RR01	NONE
v		A8	ALTIMIRA02RR01P01	J061	02RR01P01J061	ALTIMIRA02RR01	NONE
v		A10	ALTIMIRA02RR01P01	J062	02RR01P01J062	ALTIMIRA02RR01	NONE
v		A10	ALTIMIRA02RR01P01	J063	02RR01P01J063	ALTIMIRA02RR01	NONE
v		A10	ALTIMIRA02RR01P01	J064	02RR01P01J064	ALTIMIRA02RR01	NONE
v		A10	ALTIMIRA02RR01P01	J065	02RR01P01J065	ALTIMIRA02RR01	NONE
v		A12	ALTIMIRA02RR01P01	J066	02RR01P01J066	ALTIMIRA02RR01	NONE
v		A12	ALTIMIRA02RR01P01	J067	02RR01P01J067	ALTIMIRA02RR01	NONE
v		A12	ALTIMIRA02RR01P01	J068	02RR01P01J068	ALTIMIRA02RR01	NONE
v		A12	ALTIMIRA02RR01P01	J069	02RR01P01J069	ALTIMIRA02RR01	NONE
v		A1	ALTIMIRA03RR01P01	J001	03RR01P01J001	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J002	03RR01P01J002	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J003	03RR01P01J003	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J004	03RR01P01J004	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J005	03RR01P01J005	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J006	03RR01P01J006	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J007	03RR01P01J007	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J008	03RR01P01J008	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J009	03RR01P01J009	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J010	03RR01P01J010	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J011	03RR01P01J011	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J012	03RR01P01J012	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J013	03RR01P01J013	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J014	03RR01P01J014	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J015	03RR01P01J015	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J016	03RR01P01J016	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J017	03RR01P01J017	ALTIMIRA03RR01	NONE

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v		A1	ALTIMIRA03RR01P01	J018	03RR01P01J018	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J019	03RR01P01J019	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J020	03RR01P01J020	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J021	03RR01P01J021	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J022	03RR01P01J022	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J023	03RR01P01J023	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J024	03RR01P01J024	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J025	03RR01P02J025	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J026	03RR01P02J026	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J027	03RR01P02J027	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J028	03RR01P02J028	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J029	03RR01P02J029	ALTIMIRA03RR01	NONE
v		A1	ALTIMIRA03RR01P01	J030	03RR01P02J030	ALTIMIRA03RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J001	01RR01P01J001	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J002	01RR01P01J002	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J003	01RR01P01J003	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J004	01RR01P01J004	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J005	01RR01P01J005	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J006	01RR01P01J006	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J007	01RR01P01J007	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J008	01RR01P01J008	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J009	01RR01P01J009	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J010	01RR01P01J010	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J011	01RR01P01J011	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J012	01RR01P01J012	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J013	01RR01P01J013	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J014	01RR01P01J014	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J015	01RR01P01J015	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J016	01RR01P01J016	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J017	01RR01P01J017	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J018	01RR01P01J018	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J019	01RR01P01J019	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J020	01RR01P01J020	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J021	01RR01P01J021	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J022	01RR01P01J022	ALTIMIRA01RR01	NONE

<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
v		ADMIN	ALTIMIRA01RR01P01	J023	01RR01P01J023	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J024	01RR01P01J024	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J025	01RR01P02J025	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J026	01RR01P02J026	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J027	01RR01P02J027	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J028	01RR01P02J028	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J029	01RR01P02J029	ALTIMIRA01RR01	NONE
v		ADMIN	ALTIMIRA01RR01P01	J030	01RR01P02J030	ALTIMIRA01RR01	NONE



62.5/125 Multimode Fiber Optic Cable



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REVIEWED	APPROVED
BENEDICT SIGUA	ARNOLD DAMIAN, RCDD
STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008

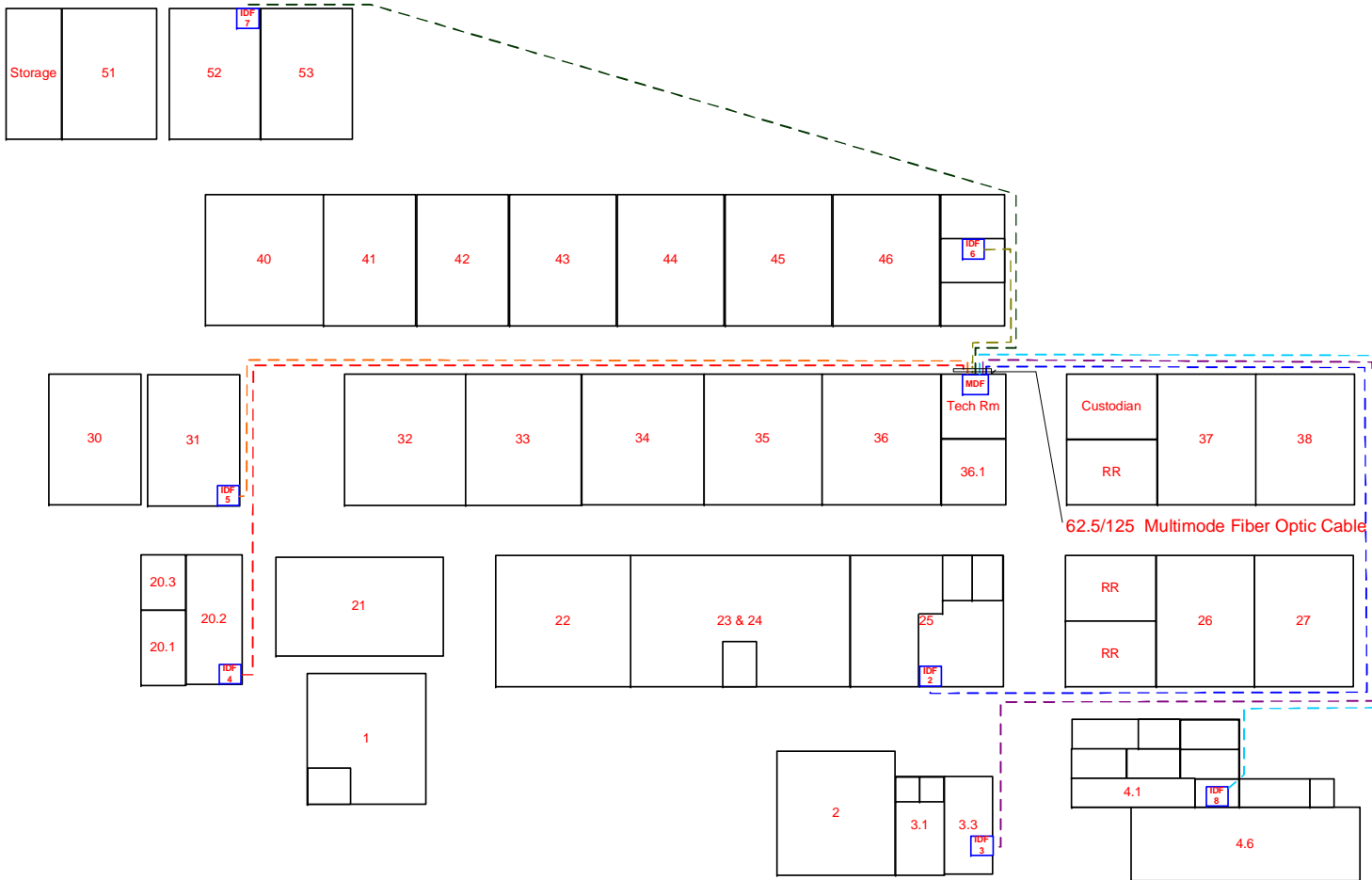
<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch</u>	<u>Port Number (Station</u>	<u>MDF/IDF</u>	<u>Comments</u>
					<u>panel side)</u>	<u>side)</u>		
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J001	03RR01P01J001	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J002	03RR01P01J002	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J003	03RR01P01J003	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J004	03RR01P01J004	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J005	03RR01P01J005	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J006	03RR01P01J006	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J007	03RR01P01J007	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J008	03RR01P01J008	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J009	03RR01P01J009	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J010	03RR01P01J010	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J011	03RR01P01J011	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J012	03RR01P01J012	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J013	03RR01P01J013	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J014	03RR01P01J014	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J015	03RR01P01J015	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J016	03RR01P01J016	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J017	03RR01P01J017	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J018	03RR01P01J018	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J019	03RR01P01J019	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J020	03RR01P01J020	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J021	03RR01P01J021	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J022	03RR01P01J022	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J023	03RR01P01J023	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P01	J024	03RR01P01J024	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J025	03RR01P02J025	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J026	03RR01P02J026	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J027	03RR01P02J027	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J028	03RR01P02J028	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J029	03RR01P02J029	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J030	03RR01P02J030	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J031	03RR01P02J031	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J032	03RR01P02J032	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J033	03RR01P02J033	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J034	03RR01P02J034	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J035	03RR01P02J035	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J036	03RR01P02J036	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J037	03RR01P02J037	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J038	03RR01P02J038	DUNBAR03RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J039	03RR01P02J039	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J040	03RR01P02J040	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J041	03RR01P02J041	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J042	03RR01P02J042	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J043	03RR01P02J043	DUNBAR03RR01	NONE
DUNBAR	√		Bldg D (Lab/Lib)	DUNBAR03RR01P02	J044	03RR01P02J044	DUNBAR03RR01	NONE
DUNBAR	√		Bldg K (Rm17)	DUNBAR05RR01P01	J001	05RR01P01J001	DUNBAR05RR01	NONE
DUNBAR	√		Bldg K (Rm17)	DUNBAR05RR01P01	J002	05RR01P01J002	DUNBAR05RR01	NONE
DUNBAR	√		Bldg K (Rm17)	DUNBAR05RR01P01	J003	05RR01P01J003	DUNBAR05RR01	NONE
DUNBAR	√		Bldg K (Rm17)	DUNBAR05RR01P01	J004	05RR01P01J004	DUNBAR05RR01	NONE
DUNBAR	√		Bldg K (Rm17)	DUNBAR05RR01P01	J005	05RR01P01J005	DUNBAR05RR01	NONE
DUNBAR	√		Bldg K (Rm17)	DUNBAR05RR01P01	J006	05RR01P01J006	DUNBAR05RR01	NONE
								NONE
DUNBAR	√		Bldg L (Rm18)	DUNBAR06RR01P01	J001	06RR01P01J001	DUNBAR06RR01	NONE
DUNBAR	√		Bldg L (Rm18)	DUNBAR06RR01P01	J002	06RR01P01J002	DUNBAR06RR01	NONE
DUNBAR	√		Bldg L (Rm18)	DUNBAR06RR01P01	J003	06RR01P01J003	DUNBAR06RR01	NONE
DUNBAR	√		Bldg L (Rm18)	DUNBAR06RR01P01	J004	06RR01P01J004	DUNBAR06RR01	NONE
DUNBAR	√		Bldg L (Rm18)	DUNBAR06RR01P01	J005	06RR01P01J005	DUNBAR06RR01	NONE
DUNBAR	√		Bldg L (Rm18)	DUNBAR06RR01P01	J006	06RR01P01J006	DUNBAR06RR01	NONE
								NONE
DUNBAR	√		Bldg M (Rm19)	DUNBAR07RR01P01	J001	07RR01P01J001	DUNBAR07RR01	NONE
DUNBAR	√		Bldg M (Rm19)	DUNBAR07RR01P01	J002	07RR01P01J002	DUNBAR07RR01	NONE
DUNBAR	√		Bldg M (Rm19)	DUNBAR07RR01P01	J003	07RR01P01J003	DUNBAR07RR01	NONE
DUNBAR	√		Bldg M (Rm19)	DUNBAR07RR01P01	J004	07RR01P01J004	DUNBAR07RR01	NONE
DUNBAR	√		Bldg M (Rm19)	DUNBAR07RR01P01	J005	07RR01P01J005	DUNBAR07RR01	NONE
DUNBAR	√		Bldg M (Rm19)	DUNBAR07RR01P01	J006	07RR01P01J006	DUNBAR07RR01	NONE
DUNBAR	√		Bldg I (Rm15)	DUNBAR08RR01P01	J001	08RR01P01J001	DUNBAR08RR01	NONE
DUNBAR	√		Bldg G (Rm14B)	DUNBAR08RR01P01	J002	08RR01P01J002	DUNBAR08RR01	Too long
DUNBAR	√		Bldg G (Rm13)	DUNBAR08RR01P01	J003	08RR01P01J003	DUNBAR08RR01	Too long
DUNBAR	√		Bldg B (Rm8)	DUNBAR02RR01P01	J001	02RR01P01J001	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm8)	DUNBAR02RR01P01	J002	02RR01P01J002	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm8)	DUNBAR02RR01P01	J003	02RR01P01J003	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm8)	DUNBAR02RR01P01	J004	02RR01P01J004	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm8)	DUNBAR02RR01P01	J005	02RR01P01J005	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm8)	DUNBAR02RR01P01	J006	02RR01P01J006	DUNBAR02RR01	NONE



<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
DUNBAR	√		Bldg B (Rm10)	DUNBAR02RR01P01	J007	02RR01P01J007	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm10)	DUNBAR02RR01P01	J008	02RR01P01J008	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm10)	DUNBAR02RR01P01	J009	02RR01P01J009	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm10)	DUNBAR02RR01P01	J010	02RR01P01J010	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm10)	DUNBAR02RR01P01	J011	02RR01P01J011	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm10)	DUNBAR02RR01P01	J012	02RR01P01J012	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm9)	DUNBAR02RR01P01	J013	02RR01P01J013	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm9)	DUNBAR02RR01P01	J014	02RR01P01J014	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm9)	DUNBAR02RR01P01	J015	02RR01P01J015	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm9)	DUNBAR02RR01P01	J016	02RR01P01J016	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm9)	DUNBAR02RR01P01	J017	02RR01P01J017	DUNBAR02RR01	NONE
DUNBAR	√		Bldg B (Rm9)	DUNBAR02RR01P01	J018	02RR01P01J018	DUNBAR02RR01	NONE
DUNBAR	√		Bldg C (Rm7)	DUNBAR02RR01P01	J019	02RR01P01J019	DUNBAR02RR01	NONE
DUNBAR	√		Bldg C (Rm7)	DUNBAR02RR01P01	J020	02RR01P01J020	DUNBAR02RR01	NONE
DUNBAR	√		Bldg C (Rm7)	DUNBAR02RR01P01	J021	02RR01P01J021	DUNBAR02RR01	NONE
DUNBAR	√		Bldg C (Rm7)	DUNBAR02RR01P01	J022	02RR01P01J022	DUNBAR02RR01	NONE
DUNBAR	√		Bldg C (Rm7)	DUNBAR02RR01P01	J023	02RR01P01J023	DUNBAR02RR01	NONE
DUNBAR	√		Bldg C (Rm7)	DUNBAR02RR01P01	J024	02RR01P01J024	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm11)	DUNBAR02RR01P01	J025	02RR01P01J025	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm11)	DUNBAR02RR01P01	J026	02RR01P01J026	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm11)	DUNBAR02RR01P01	J027	02RR01P01J027	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm11)	DUNBAR02RR01P01	J028	02RR01P01J028	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm11)	DUNBAR02RR01P01	J029	02RR01P01J029	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm11)	DUNBAR02RR01P01	J030	02RR01P01J030	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm12)	DUNBAR02RR01P01	J031	02RR01P01J031	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm12)	DUNBAR02RR01P01	J032	02RR01P01J032	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm12)	DUNBAR02RR01P01	J033	02RR01P01J033	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm12)	DUNBAR02RR01P01	J034	02RR01P01J034	DUNBAR02RR01	NONE
DUNBAR	√		Bldg E (Rm12)	DUNBAR02RR01P01	J035	02RR01P01J035	DUNBAR02RR01	NONE
DUNBAR	√		Bldg F (Rm1)	DUNBAR04RR01P01	J001	04RR01P01J001	DUNBAR04RR01	NONE
DUNBAR	√		Bldg F (Rm1)	DUNBAR04RR01P01	J002	04RR01P01J002	DUNBAR04RR01	NONE
DUNBAR	√		Bldg F (Rm1)	DUNBAR04RR01P01	J003	04RR01P01J003	DUNBAR04RR01	NONE
DUNBAR	√		Bldg F (Rm1)	DUNBAR04RR01P01	J004	04RR01P01J004	DUNBAR04RR01	NONE
DUNBAR	√		Bldg F (Rm1)	DUNBAR04RR01P01	J005	04RR01P01J005	DUNBAR04RR01	NONE
DUNBAR	√		Bldg F (Rm2)	DUNBAR04RR01P01	J006	04RR01P01J006	DUNBAR04RR01	NONE
DUNBAR	√		Bldg F (Rm2)	DUNBAR04RR01P01	J007	04RR01P01J007	DUNBAR04RR01	NONE
DUNBAR	√		Bldg F (Rm2)	DUNBAR04RR01P01	J008	04RR01P01J008	DUNBAR04RR01	NONE





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STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
EL VERANO	v		46	ELVERANO06RR01P01	J001	06RR01P01J001	ELVERANO06RR01	NONE
EL VERANO	v		46	ELVERANO06RR01P01	J002	06RR01P01J002	ELVERANO06RR01	NONE
EL VERANO	v		46	ELVERANO06RR01P01	J003	06RR01P01J003	ELVERANO06RR01	NONE
EL VERANO	v		46	ELVERANO06RR01P01	J004	06RR01P01J004	ELVERANO06RR01	NONE
EL VERANO	v		45	ELVERANO06RR01P01	J005	06RR01P01J005	ELVERANO06RR01	NONE
EL VERANO	v		45	ELVERANO06RR01P01	J006	06RR01P01J006	ELVERANO06RR01	NONE
EL VERANO	v		45	ELVERANO06RR01P01	J007	06RR01P01J007	ELVERANO06RR01	NONE
EL VERANO	v		45	ELVERANO06RR01P01	J008	06RR01P01J008	ELVERANO06RR01	NONE
EL VERANO	v		44	ELVERANO06RR01P01	J009	06RR01P01J009	ELVERANO06RR01	NONE
EL VERANO	v		44	ELVERANO06RR01P01	J010	06RR01P01J010	ELVERANO06RR01	NONE
EL VERANO	v		44	ELVERANO06RR01P01	J011	06RR01P01J011	ELVERANO06RR01	NONE
EL VERANO	v		44	ELVERANO06RR01P01	J012	06RR01P01J012	ELVERANO06RR01	NONE
EL VERANO	v		43	ELVERANO06RR01P01	J013	06RR01P01J013	ELVERANO06RR01	NONE
EL VERANO	v		43	ELVERANO06RR01P01	J014	06RR01P01J014	ELVERANO06RR01	NONE
EL VERANO	v		43	ELVERANO06RR01P01	J015	06RR01P01J015	ELVERANO06RR01	NONE
EL VERANO	v		43	ELVERANO06RR01P01	J016	06RR01P01J016	ELVERANO06RR01	NONE
EL VERANO	v		42	ELVERANO06RR01P01	J017	06RR01P01J017	ELVERANO06RR01	NONE
EL VERANO	v		42	ELVERANO06RR01P01	J018	06RR01P01J018	ELVERANO06RR01	NONE
EL VERANO	v		42	ELVERANO06RR01P01	J019	06RR01P01J019	ELVERANO06RR01	NONE
EL VERANO	v		42	ELVERANO06RR01P01	J020	06RR01P01J020	ELVERANO06RR01	NONE
EL VERANO	v		41	ELVERANO06RR01P01	J021	06RR01P01J021	ELVERANO06RR01	NONE
EL VERANO	v		41	ELVERANO06RR01P01	J022	06RR01P01J022	ELVERANO06RR01	NONE
EL VERANO	v		41	ELVERANO06RR01P01	J023	06RR01P01J023	ELVERANO06RR01	NONE
EL VERANO	v		41	ELVERANO06RR01P01	J024	06RR01P01J024	ELVERANO06RR01	NONE
EL VERANO	v		40	ELVERANO06RR01P01	J033	06RR01P01J033	ELVERANO06RR01	NONE
EL VERANO	v		40	ELVERANO06RR01P01	J034	06RR01P01J034	ELVERANO06RR01	NONE
EL VERANO	v		40	ELVERANO06RR01P01	J035	06RR01P01J035	ELVERANO06RR01	NONE
EL VERANO	v		40	ELVERANO06RR01P01	J036	06RR01P01J036	ELVERANO06RR01	NONE
EL VERANO	v		53	ELVERANO07RR01P01	J001	07RR01P01J001	ELVERANO07RR01	NONE
EL VERANO	v		53	ELVERANO07RR01P01	J002	07RR01P01J002	ELVERANO07RR01	NONE
EL VERANO	v		53	ELVERANO07RR01P01	J003	07RR01P01J003	ELVERANO07RR01	NONE
EL VERANO	v		53	ELVERANO07RR01P01	J004	07RR01P01J004	ELVERANO07RR01	NONE
EL VERANO	v		51	ELVERANO09RR01P01	J001	09RR01P01J001	ELVERANO09RR01	NONE
EL VERANO	v		51	ELVERANO09RR01P01	J002	09RR01P01J002	ELVERANO09RR01	NONE
EL VERANO	v		51	ELVERANO09RR01P01	J003	09RR01P01J003	ELVERANO09RR01	NONE
EL VERANO	v		51	ELVERANO09RR01P01	J004	09RR01P01J004	ELVERANO09RR01	NONE
EL VERANO	v		52	ELVERANO09RR01P01	J005	09RR01P01J005	ELVERANO09RR01	NONE
EL VERANO	v		52	ELVERANO09RR01P01	J006	09RR01P01J006	ELVERANO09RR01	NONE

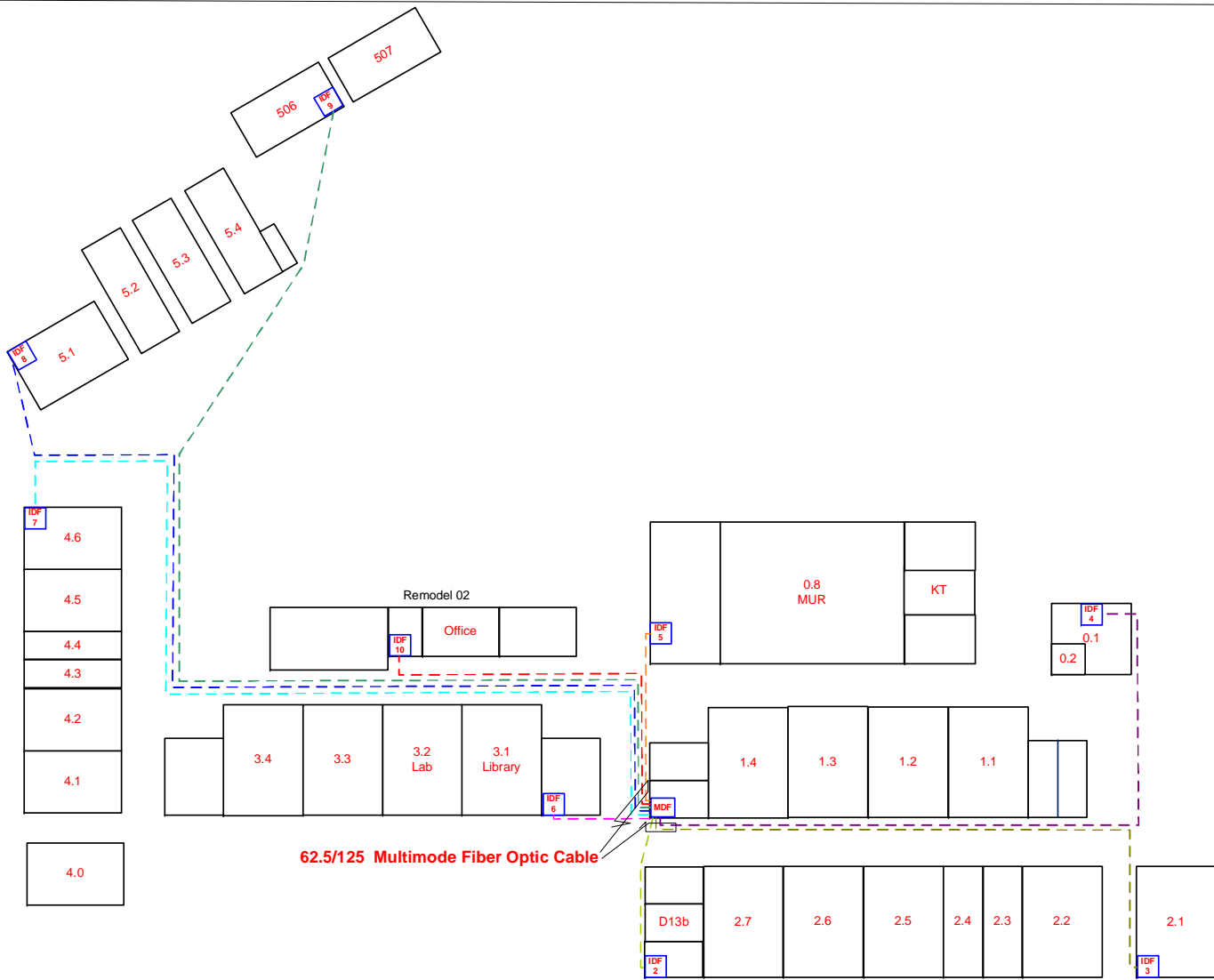
<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
EL VERANO	v		52	ELVERANO09RR01P01	J007	09RR01P01J007	ELVERANO09RR01	NONE
EL VERANO	v		52	ELVERANO09RR01P01	J008	09RR01P01J008	ELVERANO09RR01	NONE
EL VERANO	v		37	ELVERANO01RR01P01	J001	01RR01P01J001	ELVERANO01RR01	NONE
EL VERANO	v		37	ELVERANO01RR01P01	J002	01RR01P01J002	ELVERANO01RR01	NONE
EL VERANO	v		37	ELVERANO01RR01P01	J003	01RR01P01J003	ELVERANO01RR01	NONE
EL VERANO	v		37	ELVERANO01RR01P01	J004	01RR01P01J004	ELVERANO01RR01	NONE
EL VERANO	v		38	ELVERANO01RR01P01	J005	01RR01P01J005	ELVERANO01RR01	NONE
EL VERANO	v		38	ELVERANO01RR01P01	J006	01RR01P01J006	ELVERANO01RR01	NONE
EL VERANO	v		38	ELVERANO01RR01P01	J007	01RR01P01J007	ELVERANO01RR01	NONE
EL VERANO	v		38	ELVERANO01RR01P01	J008	01RR01P01J008	ELVERANO01RR01	NONE
EL VERANO	v		36	ELVERANO01RR01P01	J009	01RR01P01J009	ELVERANO01RR01	NONE
EL VERANO	v		36	ELVERANO01RR01P01	J010	01RR01P01J010	ELVERANO01RR01	NONE
EL VERANO	v		36	ELVERANO01RR01P01	J011	01RR01P01J011	ELVERANO01RR01	NONE
EL VERANO	v		36	ELVERANO01RR01P01	J012	01RR01P01J012	ELVERANO01RR01	NONE
EL VERANO	v		35	ELVERANO01RR01P01	J013	01RR01P01J013	ELVERANO01RR01	NONE
EL VERANO	v		35	ELVERANO01RR01P01	J014	01RR01P01J014	ELVERANO01RR01	NONE
EL VERANO	v		35	ELVERANO01RR01P01	J015	01RR01P01J015	ELVERANO01RR01	NONE
EL VERANO	v		35	ELVERANO01RR01P01	J016	01RR01P01J016	ELVERANO01RR01	NONE
EL VERANO	v		34	ELVERANO01RR01P01	J017	01RR01P01J017	ELVERANO01RR01	NONE
EL VERANO	v		34	ELVERANO01RR01P01	J018	01RR01P01J018	ELVERANO01RR01	NONE
EL VERANO	v		34	ELVERANO01RR01P01	J019	01RR01P01J019	ELVERANO01RR01	NONE
EL VERANO	v		34	ELVERANO01RR01P01	J020	01RR01P01J020	ELVERANO01RR01	NONE
EL VERANO	v		33	ELVERANO01RR01P01	J021	01RR01P01J021	ELVERANO01RR01	NONE
EL VERANO	v		33	ELVERANO01RR01P01	J022	01RR01P01J022	ELVERANO01RR01	NONE
EL VERANO	v		33	ELVERANO01RR01P01	J023	01RR01P01J023	ELVERANO01RR01	NONE
EL VERANO	v		33	ELVERANO01RR01P01	J024	01RR01P01J024	ELVERANO01RR01	NONE
EL VERANO	v		32	ELVERANO01RR01P01	J025	01RR01P01J025	ELVERANO01RR01	NONE
EL VERANO	v		32	ELVERANO01RR01P01	J026	01RR01P01J026	ELVERANO01RR01	NONE
EL VERANO	v		32	ELVERANO01RR01P01	J027	01RR01P01J027	ELVERANO01RR01	NONE
EL VERANO	v		32	ELVERANO01RR01P01	J028	01RR01P01J028	ELVERANO01RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J001	02RR01P01J001	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J002	02RR01P01J002	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J003	02RR01P01J003	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J004	02RR01P01J004	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J005	02RR01P01J005	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J006	02RR01P01J006	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J007	02RR01P01J007	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J008	02RR01P01J008	ELVERANO02RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
EL VERANO	v		24	ELVERANO02RR01P01	J009	02RR01P01J009	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J010	02RR01P01J010	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J011	02RR01P01J011	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J012	02RR01P01J012	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J013	02RR01P01J013	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J014	02RR01P01J014	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J015	02RR01P01J015	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J016	02RR01P01J016	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J017	02RR01P01J017	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J018	02RR01P01J018	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J019	02RR01P01J019	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J020	02RR01P01J020	ELVERANO02RR01	NONE
EL VERANO	v		24	ELVERANO02RR01P01	J021	02RR01P01J021	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J022	02RR01P01J022	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J023	02RR01P01J023	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J024	02RR01P01J024	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J025	02RR01P01J025	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J026	02RR01P01J026	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J027	02RR01P01J027	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J028	02RR01P01J028	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J029	02RR01P01J029	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J030	02RR01P01J030	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J031	02RR01P01J031	ELVERANO02RR01	NONE
EL VERANO	v		23	ELVERANO02RR01P01	J032	02RR01P01J032	ELVERANO02RR01	NONE
EL VERANO	v		26	ELVERANO02RR01P01	J033	02RR01P01J033	ELVERANO02RR01	NONE
EL VERANO	v		26	ELVERANO02RR01P01	J034	02RR01P01J034	ELVERANO02RR01	NONE
EL VERANO	v		26	ELVERANO02RR01P01	J035	02RR01P01J035	ELVERANO02RR01	NONE
EL VERANO	v		26	ELVERANO02RR01P01	J036	02RR01P01J036	ELVERANO02RR01	NONE
EL VERANO	v		26	ELVERANO02RR01P01	J037	02RR01P01J037	ELVERANO02RR01	NONE
EL VERANO	v		27	ELVERANO02RR01P01	J038	02RR01P01J038	ELVERANO02RR01	NONE
EL VERANO	v		27	ELVERANO02RR01P01	J039	02RR01P01J039	ELVERANO02RR01	NONE
EL VERANO	v		27	ELVERANO02RR01P01	J040	02RR01P01J040	ELVERANO02RR01	NONE
EL VERANO	v		27	ELVERANO02RR01P01	J041	02RR01P01J041	ELVERANO02RR01	NONE
EL VERANO	v		3.1	ELVERANO03RR01P01	J001	03RR01P01J001	ELVERANO03RR01	NONE
EL VERANO	v		3.2	ELVERANO03RR01P01	J002	03RR01P01J002	ELVERANO03RR01	NONE
EL VERANO	v		3.3	ELVERANO03RR01P01	J003	03RR01P01J003	ELVERANO03RR01	NONE
EL VERANO	v		2.2	ELVERANO03RR01P01	J004	03RR01P01J004	ELVERANO03RR01	NONE
EL VERANO	v		2.2	ELVERANO03RR01P01	J005	03RR01P01J005	ELVERANO03RR01	NONE
EL VERANO	v		2.2	ELVERANO03RR01P01	J006	03RR01P01J006	ELVERANO03RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
EL VERANO	v		2.2	ELVERANO03RR01P01	J007	03RR01P01J007	ELVERANO03RR01	NONE
EL VERANO	v		2.2	ELVERANO03RR01P01	J008	03RR01P01J008	ELVERANO03RR01	NONE
EL VERANO	v		21	ELVERANO03RR01P01	J009	03RR01P01J009	ELVERANO03RR01	NONE
EL VERANO	v		21	ELVERANO03RR01P01	J010	03RR01P01J010	ELVERANO03RR01	NONE
EL VERANO	v		21	ELVERANO03RR01P01	J011	03RR01P01J011	ELVERANO03RR01	NONE
EL VERANO	v		21	ELVERANO03RR01P01	J012	03RR01P01J012	ELVERANO03RR01	NONE
EL VERANO	v		1	ELVERANO03RR01P01	J013	03RR01P01J013	ELVERANO03RR01	NONE
EL VERANO	v		1	ELVERANO03RR01P01	J014	03RR01P01J014	ELVERANO03RR01	NONE
EL VERANO	v		1	ELVERANO03RR01P01	J015	03RR01P01J015	ELVERANO03RR01	NONE
EL VERANO	v		20.2	ELVERANO03RR01P01	J001	03RR01P01J001	ELVERANO03RR01	NONE
EL VERANO	v		20.2	ELVERANO03RR01P01	J002	03RR01P01J002	ELVERANO03RR01	NONE
EL VERANO	v		20.2	ELVERANO03RR01P01	J003	03RR01P01J003	ELVERANO03RR01	NONE
EL VERANO	v		20.2	ELVERANO03RR01P01	J004	03RR01P01J004	ELVERANO03RR01	NONE
EL VERANO	v		20.1	ELVERANO03RR01P01	J005	03RR01P01J005	ELVERANO03RR01	NONE
EL VERANO	v		20.1	ELVERANO03RR01P01	J006	03RR01P01J006	ELVERANO03RR01	NONE
EL VERANO	v		20.1	ELVERANO03RR01P01	J007	03RR01P01J007	ELVERANO03RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J001		ELVERANO08RR01	Can't locate
EL VERANO	v		4.1	ELVERANO08RR01P01	J002	08RR01P01J002	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J003	08RR01P01J003	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J004	08RR01P01J004	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J005	08RR01P01J005	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J006	08RR01P01J006	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J007		ELVERANO08RR01	Can't locate
EL VERANO	v		4.1	ELVERANO08RR01P01	J008	08RR01P01J008	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J009	08RR01P01J009	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J010	08RR01P01J010	ELVERANO08RR01	NONE
EL VERANO	v		4.1	ELVERANO08RR01P01	J011		ELVERANO08RR01	Can't locate
EL VERANO	v		4.1	ELVERANO08RR01P01	J012	08RR01P01J012	ELVERANO08RR01	NONE
EL VERANO	v		4.6	ELVERANO08RR01P01	J013		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J014		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J015		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J016		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J017		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J018		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J019		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J020		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J021		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J022		ELVERANO08RR01	Can't locate

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
EL VERANO	v		4.6	ELVERANO08RR01P01	J023		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J024		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J025	08RR01P01J025	ELVERANO08RR01	NONE
EL VERANO	v		4.6	ELVERANO08RR01P01	J026	08RR01P01J026	ELVERANO08RR01	NONE
EL VERANO	v		4.6	ELVERANO08RR01P01	J027		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J028		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J029		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J030		ELVERANO08RR01	Can't locate
EL VERANO	v		4.6	ELVERANO08RR01P01	J031	08RR01P01J031	ELVERANO08RR01	NONE
EL VERANO	v		4.6	ELVERANO08RR01P01	J032	08RR01P01J032	ELVERANO08RR01	NONE
EL VERANO	v		4.6	ELVERANO08RR01P01	J033	08RR01P01J033	ELVERANO08RR01	NONE





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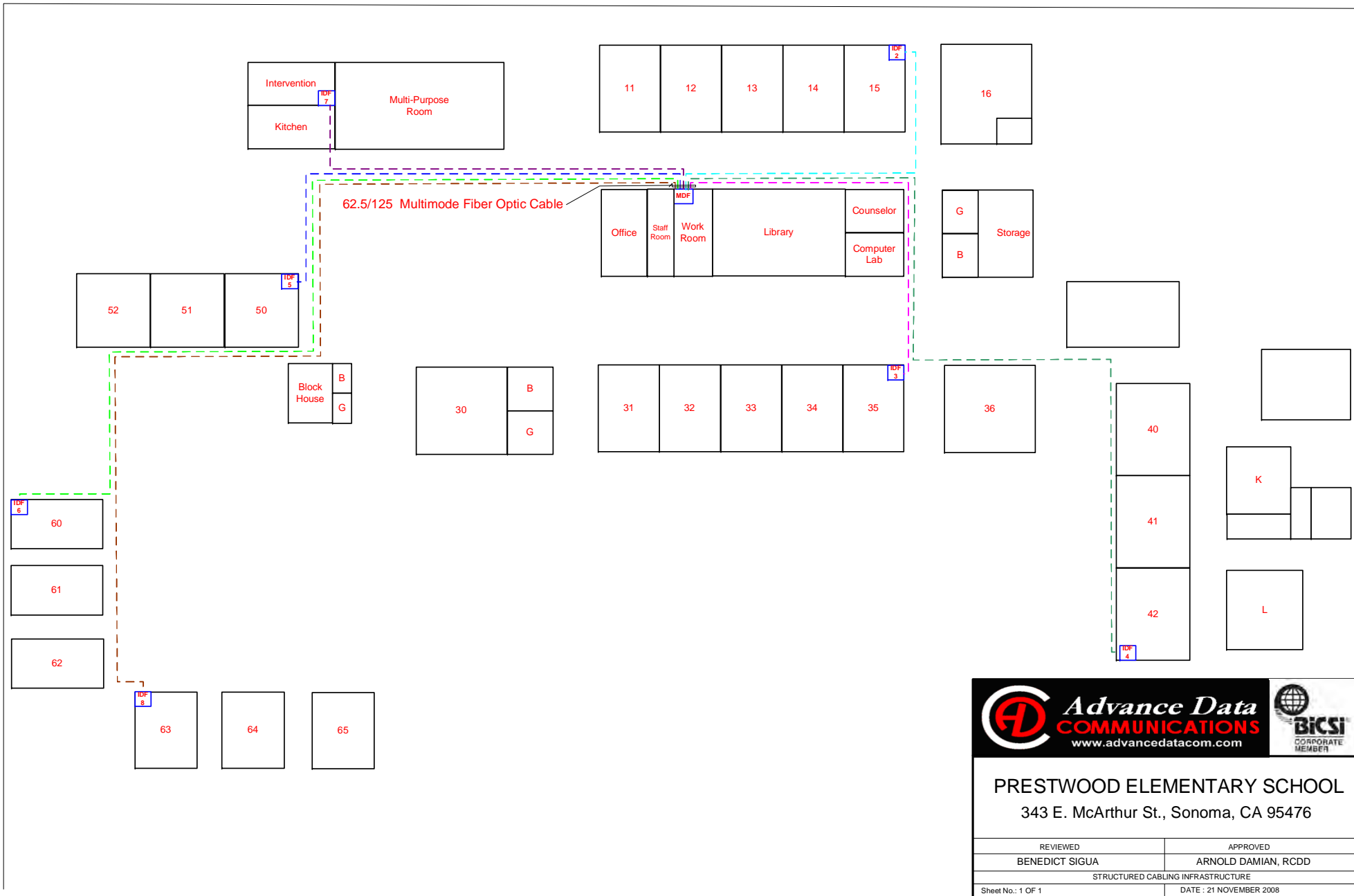
REVIEWED	APPROVED
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STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
FLOWERY	v		3.1	FLOWERY06RR01P01	J001	03RR01P01J001	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J002	03RR01P01J002	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J003	03RR01P01J003	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J004	03RR01P01J004	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J005	03RR01P01J005	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J006	03RR01P01J006	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J007	03RR01P01J007	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J008	03RR01P01J008	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J009	03RR01P01J009	FLOWERY06RR01	NONE
FLOWERY	v		3.1	FLOWERY06RR01P01	J010	03RR01P01J010	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J011	03RR01P01J011	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J012	03RR01P01J012	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J013	03RR01P01J013	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J014	03RR01P01J014	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J015	03RR01P01J015	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J016	03RR01P01J016	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J017	03RR01P01J017	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J018	03RR01P01J018	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J019	03RR01P01J019	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J020	03RR01P01J020	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J021	03RR01P01J021	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J022	03RR01P01J022	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J023	03RR01P01J023	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J024	03RR01P01J024	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J025	03RR01P02J025	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J026	03RR01P02J026	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J027	03RR01P02J027	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J028	03RR01P02J028	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J029	03RR01P02J029	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J030	03RR01P02J030	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J031	03RR01P02J031	FLOWERY06RR01	NONE
FLOWERY	v		3.2	FLOWERY06RR01P01	J032	03RR01P02J032	FLOWERY06RR01	NONE
FLOWERY	v		3.3	FLOWERY06RR01P01	J033	03RR01P02J033	FLOWERY06RR01	NONE
FLOWERY	v		3.3	FLOWERY06RR01P01	J034	03RR01P02J034	FLOWERY06RR01	NONE
FLOWERY	v		3.3	FLOWERY06RR01P01	J035	03RR01P02J035	FLOWERY06RR01	NONE
FLOWERY	v		3.3	FLOWERY06RR01P01	J036	03RR01P02J036	FLOWERY06RR01	NONE
FLOWERY	v		3.4	FLOWERY06RR01P01	J037	03RR01P02J037	FLOWERY06RR01	NONE
FLOWERY	v		3.4	FLOWERY06RR01P01	J038	03RR01P02J038	FLOWERY06RR01	NONE
FLOWERY	v		3.4	FLOWERY06RR01P01	J039	03RR01P02J039	FLOWERY06RR01	NONE
FLOWERY	v		3.4	FLOWERY06RR01P01	J040	03RR01P02J040	FLOWERY06RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
FLOWERY	v		2-7	FLOWERY02RR01P01	J001	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.7	FLOWERY02RR01P01	J002	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.7	FLOWERY02RR01P01	J003	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.7	FLOWERY02RR01P01	J004	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.6	FLOWERY02RR01P01	J005	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.6	FLOWERY02RR01P01	J006	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY			2.6	FLOWERY02RR01P01	J007	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.6	FLOWERY02RR01P01	J008	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.5	FLOWERY02RR01P01	J009	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.5	FLOWERY02RR01P01	J010	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.5	FLOWERY02RR01P01	J011	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.5	FLOWERY02RR01P01	J012	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.4	FLOWERY02RR01P01	J013	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY			2.4	FLOWERY02RR01P01	J014	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.4	FLOWERY02RR01P01	J015	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.4	FLOWERY02RR01P01	J016	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.3	FLOWERY02RR01P01	J017	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.3	FLOWERY02RR01P01	J018	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.3	FLOWERY02RR01P01	J019	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.3	FLOWERY02RR01P01	J020	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.2	FLOWERY02RR01P01	J021	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.2	FLOWERY02RR01P01	J022	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.2	FLOWERY02RR01P01	J023	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		2.2	FLOWERY02RR01P01	J024	02RR01P01J001	FLOWERY02RR01	NONE
FLOWERY	v		4.6	FLOWERY07RR01P01	J001	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.6	FLOWERY07RR01P01	J002	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.6	FLOWERY07RR01P01	J003	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.6	FLOWERY07RR01P01	J004	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.5	FLOWERY07RR01P01	J005	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.5	FLOWERY07RR01P01	J006	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.5	FLOWERY07RR01P01	J007	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.5	FLOWERY07RR01P01	J008	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.4	FLOWERY07RR01P01	J009	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.3	FLOWERY07RR01P01	J010	07RR01P01J001	FLOWERY07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
FLOWERY	v		4.2	FLOWERY07RR01P01	J011	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.2	FLOWERY07RR01P01	J012	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.2	FLOWERY07RR01P01	J013	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.2	FLOWERY07RR01P01	J014	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.1	FLOWERY07RR01P01	J015	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.1	FLOWERY07RR01P01	J016	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.1	FLOWERY07RR01P01	J017	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		4.1	FLOWERY07RR01P01	J018	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v			FLOWERY07RR01P01	J019	07RR01P01J001	FLOWERY07RR01	not located
FLOWERY	v			FLOWERY07RR01P01	J020	07RR01P01J001	FLOWERY07RR01	not located
FLOWERY	v		4	FLOWERY07RR01P01	J021	07RR01P01J001	FLOWERY07RR01	no tone
FLOWERY	v		4	FLOWERY07RR01P01	J022	07RR01P01J001	FLOWERY07RR01	no tone
FLOWERY	v		4	FLOWERY07RR01P01	J023	07RR01P01J001	FLOWERY07RR01	no tone
FLOWERY	v		4	FLOWERY07RR01P01	J024	07RR01P01J001	FLOWERY07RR01	NONE
FLOWERY	v		5.1	FLOWERY08RR01P01	J001	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.1	FLOWERY08RR01P01	J002	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.1	FLOWERY08RR01P01	J003	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.1	FLOWERY08RR01P01	J004	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.2	FLOWERY08RR01P01	J005	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.2	FLOWERY08RR01P01	J006	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.2	FLOWERY08RR01P01	J007	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.2	FLOWERY08RR01P01	J008	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.3	FLOWERY08RR01P01	J009	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.3	FLOWERY08RR01P01	J010	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.3	FLOWERY08RR01P01	J011	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.3	FLOWERY08RR01P01	J012	08RR01P01J001	FLOWERY08RR01	NONE
FLOWERY	v		5.4	FLOWERY08RR01P01	J013	08RR01P01J001	FLOWERY08RR01	no access
FLOWERY	v		5.4	FLOWERY08RR01P01	J014	08RR01P01J001	FLOWERY08RR01	no access
FLOWERY	v		5.4	FLOWERY08RR01P01	J015	08RR01P01J001	FLOWERY08RR01	no access
FLOWERY	v		5.4	FLOWERY08RR01P01	J016	08RR01P01J001	FLOWERY08RR01	no access
FLOWERY	v		1.4	FLOWERY01RR01P01	J001	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.4	FLOWERY01RR01P01	J002	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.4	FLOWERY01RR01P01	J003	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.4	FLOWERY01RR01P01	J004	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.3	FLOWERY01RR01P01	J005	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.3	FLOWERY01RR01P01	J006	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.3	FLOWERY01RR01P01	J007	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.3	FLOWERY01RR01P01	J008	01RR01P01J001	FLOWERY01RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
FLOWERY	v		1.2	FLOWERY01RR01P01	J009	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.3	FLOWERY01RR01P01	J010	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.2	FLOWERY01RR01P01	J011	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.2	FLOWERY01RR01P01	J012	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.2	FLOWERY01RR01P01	J013	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.2	FLOWERY01RR01P01	J014	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.1	FLOWERY01RR01P01	J015	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.1	FLOWERY01RR01P01	J016	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.1	FLOWERY01RR01P01	J017	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.1	FLOWERY01RR01P01	J018	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.4	FLOWERY01RR01P01	J019	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		1.4	FLOWERY01RR01P01	J020	01RR01P01J001	FLOWERY01RR01	NONE
FLOWERY	v		0.8	FLOWERY05RR01P01	J001	05RR01P01J001	FLOWERY05RR01	NONE
FLOWERY	v		0.8	FLOWERY05RR01P01	J002	05RR01P01J001	FLOWERY05RR01	NONE
FLOWERY	v		0.8	FLOWERY05RR01P01	J003	05RR01P01J001	FLOWERY05RR01	NONE
FLOWERY	v		0.8	FLOWERY05RR01P01	J004	05RR01P01J001	FLOWERY05RR01	NONE
FLOWERY	v		0.8	FLOWERY05RR01P01	J005	05RR01P01J001	FLOWERY05RR01	NONE
FLOWERY	v		0.8	FLOWERY05RR01P01	J006	05RR01P01J001	FLOWERY05RR01	NONE
FLOWERY	v		0.8	FLOWERY05RR01P01	J007	05RR01P01J001	FLOWERY05RR01	NONE



**PRESTWOOD ELEMENTARY SCHOOL**  
 343 E. McArthur St., Sonoma, CA 95476

REVIEWED	APPROVED
BENEDICT SIGUA	ARNOLD DAMIAN, RCDD
STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
PRESTWOOD	v		35	PRESTWOOD03RR01P01	J001	03RR01P02J001	PRESTWOOD03RR01	NONE
PRESTWOOD	v		35	PRESTWOOD03RR01P01	J002	03RR01P02J002	PRESTWOOD03RR01	NONE
PRESTWOOD	v		35	PRESTWOOD03RR01P01	J003	03RR01P02J003	PRESTWOOD03RR01	NONE
PRESTWOOD	v		35	PRESTWOOD03RR01P01	J004	03RR01P02J004	PRESTWOOD03RR01	NONE
PRESTWOOD	v		34	PRESTWOOD03RR01P01	J005	03RR01P02J005	PRESTWOOD03RR01	NONE
PRESTWOOD	v		34	PRESTWOOD03RR01P01	J006	03RR01P02J006	PRESTWOOD03RR01	NONE
PRESTWOOD	v		34	PRESTWOOD03RR01P01	J007	03RR01P02J007	PRESTWOOD03RR01	NONE
PRESTWOOD	v		34	PRESTWOOD03RR01P01	J008	03RR01P02J008	PRESTWOOD03RR01	NONE
PRESTWOOD	v		33	PRESTWOOD03RR01P01	J009	03RR01P02J009	PRESTWOOD03RR01	NONE
PRESTWOOD	v		33	PRESTWOOD03RR01P01	J010	03RR01P02J010	PRESTWOOD03RR01	NONE
PRESTWOOD	v		33	PRESTWOOD03RR01P01	J011	03RR01P02J011	PRESTWOOD03RR01	NONE
PRESTWOOD	v		33	PRESTWOOD03RR01P01	J012	03RR01P02J012	PRESTWOOD03RR01	NONE
PRESTWOOD	v		32	PRESTWOOD03RR01P01	J013	03RR01P02J013	PRESTWOOD03RR01	NONE
PRESTWOOD	v		32	PRESTWOOD03RR01P01	J014	03RR01P02J014	PRESTWOOD03RR01	NONE
PRESTWOOD	v		32	PRESTWOOD03RR01P01	J015	03RR01P02J015	PRESTWOOD03RR01	NONE
PRESTWOOD	v		32	PRESTWOOD03RR01P01	J016	03RR01P02J016	PRESTWOOD03RR01	NONE
PRESTWOOD	v		31	PRESTWOOD03RR01P01	J017	03RR01P02J017	PRESTWOOD03RR01	NONE
PRESTWOOD	v		31	PRESTWOOD03RR01P01	J018	03RR01P02J018	PRESTWOOD03RR01	NONE
PRESTWOOD	v		31	PRESTWOOD03RR01P01	J019	03RR01P02J019	PRESTWOOD03RR01	NONE
PRESTWOOD	v		31	PRESTWOOD03RR01P01	J020	03RR01P02J020	PRESTWOOD03RR01	NONE
PRESTWOOD	v		36	PRESTWOOD03RR01P01	J021	03RR01P02J021	PRESTWOOD03RR01	NONE
PRESTWOOD	v		36	PRESTWOOD03RR01P01	J022	03RR01P02J022	PRESTWOOD03RR01	NONE
PRESTWOOD	v		36	PRESTWOOD03RR01P01	J023	03RR01P02J023	PRESTWOOD03RR01	NONE
PRESTWOOD	v		36	PRESTWOOD03RR01P01	J024	03RR01P02J024	PRESTWOOD03RR01	NONE
PRESTWOOD	v		30	PRESTWOOD03RR01P01	J025	03RR01P02J025	PRESTWOOD03RR01	NONE
PRESTWOOD	v		30	PRESTWOOD03RR01P01	J026	03RR01P02J026	PRESTWOOD03RR01	NONE
PRESTWOOD	v		30	PRESTWOOD03RR01P01	J027	03RR01P02J027	PRESTWOOD03RR01	NONE
PRESTWOOD	v		30	PRESTWOOD03RR01P01	J028	03RR01P02J028	PRESTWOOD03RR01	NONE
PRESTWOOD	v		15	PRESTWOOD02RR01P01	J001	02RR01P02J001	PRESTWOOD02RR01	NONE
PRESTWOOD	v		15	PRESTWOOD02RR01P01	J002	02RR01P02J002	PRESTWOOD02RR01	NONE
PRESTWOOD	v		15	PRESTWOOD02RR01P01	J003	02RR01P02J003	PRESTWOOD02RR01	NONE
PRESTWOOD	v		15	PRESTWOOD02RR01P01	J004	02RR01P02J004	PRESTWOOD02RR01	NONE
PRESTWOOD	v		14	PRESTWOOD02RR01P01	J005	02RR01P02J005	PRESTWOOD02RR01	NONE
PRESTWOOD	v		14	PRESTWOOD02RR01P01	J006	02RR01P02J006	PRESTWOOD02RR01	NONE
PRESTWOOD	v		14	PRESTWOOD02RR01P01	J007	02RR01P02J007	PRESTWOOD02RR01	NONE
PRESTWOOD	v		14	PRESTWOOD02RR01P01	J008	02RR01P02J008	PRESTWOOD02RR01	NONE
PRESTWOOD	v		13	PRESTWOOD02RR01P01	J009	02RR01P02J009	PRESTWOOD02RR01	NONE
PRESTWOOD	v		13	PRESTWOOD02RR01P01	J010	02RR01P02J010	PRESTWOOD02RR01	NONE
PRESTWOOD	v		13	PRESTWOOD02RR01P01	J011	02RR01P02J011	PRESTWOOD02RR01	NONE
PRESTWOOD	v		13	PRESTWOOD02RR01P01	J012	02RR01P02J012	PRESTWOOD02RR01	NONE
PRESTWOOD	v		12	PRESTWOOD02RR01P01	J013	02RR01P02J013	PRESTWOOD02RR01	NONE
PRESTWOOD	v		12	PRESTWOOD02RR01P01	J014	02RR01P02J014	PRESTWOOD02RR01	NONE
PRESTWOOD	v		12	PRESTWOOD02RR01P01	J015	02RR01P02J015	PRESTWOOD02RR01	NONE
PRESTWOOD	v		12	PRESTWOOD02RR01P01	J016	02RR01P02J016	PRESTWOOD02RR01	NONE
PRESTWOOD	v		11	PRESTWOOD02RR01P01	J017	02RR01P02J017	PRESTWOOD02RR01	NONE

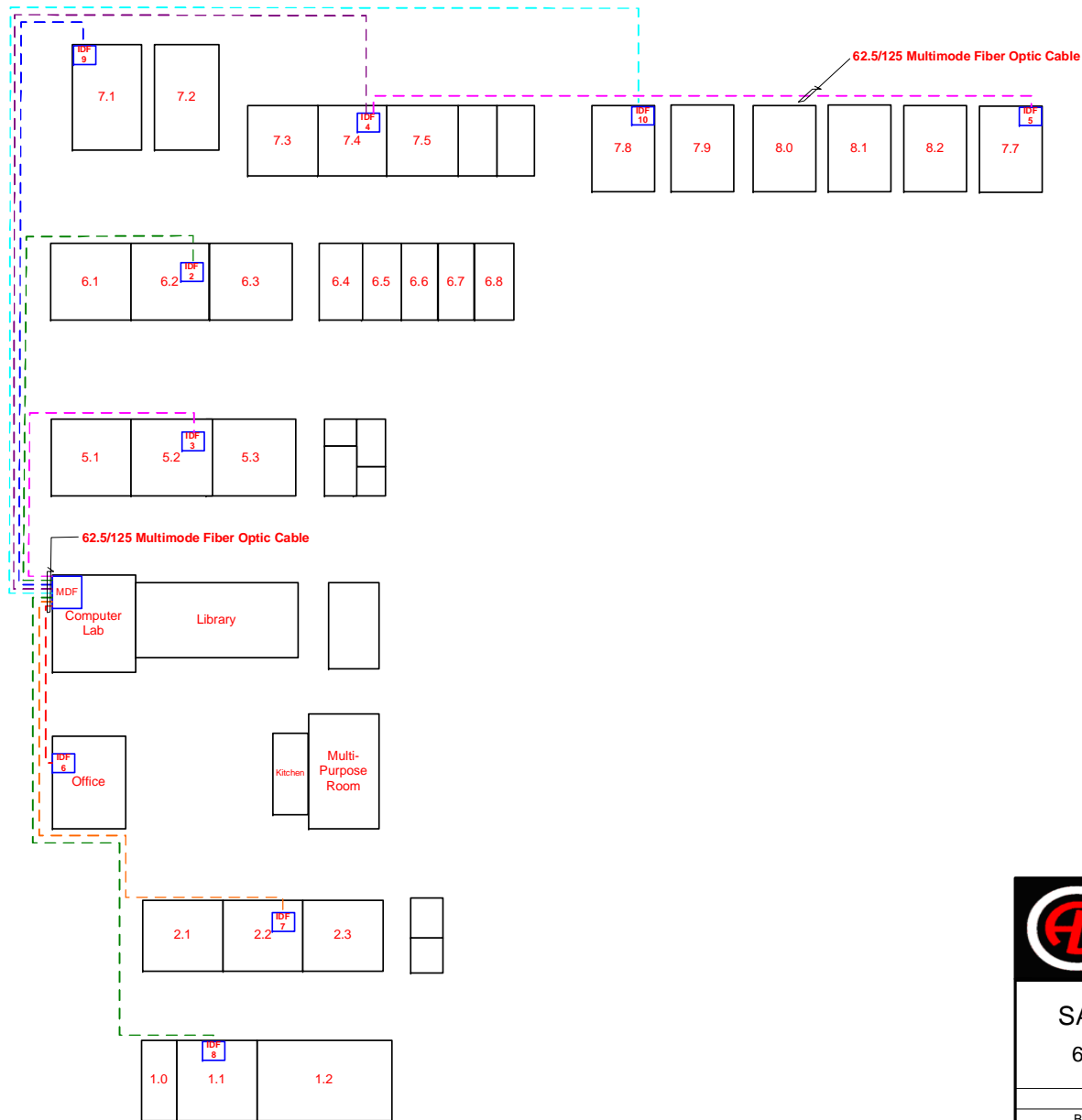
<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
PRESTWOOD	v		11	PRESTWOOD02RR01P01	J018	02RR01P02J018	PRESTWOOD02RR01	NONE
PRESTWOOD	v		11	PRESTWOOD02RR01P01	J019	02RR01P02J019	PRESTWOOD02RR01	NONE
PRESTWOOD	v		11	PRESTWOOD02RR01P01	J020	02RR01P02J020	PRESTWOOD02RR01	NONE
PRESTWOOD	v		16	PRESTWOOD03RR01P01	J020	03RR01P02J020	PRESTWOOD03RR01	NONE
PRESTWOOD	v		16	PRESTWOOD03RR01P01	J021	03RR01P02J021	PRESTWOOD03RR01	NONE
PRESTWOOD	v		16	PRESTWOOD03RR01P01	J022	03RR01P02J022	PRESTWOOD03RR01	NONE
PRESTWOOD	v		16	PRESTWOOD03RR01P01	J023	03RR01P02J023	PRESTWOOD03RR01	NONE
PRESTWOOD	v		16	PRESTWOOD03RR01P01	J024	03RR01P02J024	PRESTWOOD03RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J001	07RR01P02J001	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J002	07RR01P02J002	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J003	07RR01P02J003	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J004	07RR01P02J004	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J005	07RR01P02J005	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J006	07RR01P02J006	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J007	07RR01P02J007	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J008	07RR01P02J008	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J009	07RR01P02J009	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J010	07RR01P02J010	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J011	07RR01P02J011	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J012	07RR01P02J012	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J013	07RR01P02J013	PRESTWOOD07RR01	NONE
PRESTWOOD	v		MULTIPURPOSE ROOM	PRESTWOOD07RR01P01	J014	07RR01P02J014	PRESTWOOD07RR01	NONE
PRESTWOOD	v		40	PRESTWOOD04RR01P01	J001	04RR01P02J001	PRESTWOOD04RR01	NOT LOCATED
PRESTWOOD	v		40	PRESTWOOD04RR01P01	J002	04RR01P02J002	PRESTWOOD04RR01	NOT LOCATED
PRESTWOOD	v		40	PRESTWOOD04RR01P01	J003	04RR01P02J003	PRESTWOOD04RR01	NONE
PRESTWOOD	v		40	PRESTWOOD04RR01P01	J004	04RR01P02J004	PRESTWOOD04RR01	NONE
PRESTWOOD	v		40	PRESTWOOD04RR01P01	J005	04RR01P02J005	PRESTWOOD04RR01	NONE
PRESTWOOD	v		40	PRESTWOOD04RR01P01	J006	04RR01P02J006	PRESTWOOD04RR01	NONE
PRESTWOOD	v		40	PRESTWOOD04RR01P01	J007	04RR01P02J007	PRESTWOOD04RR01	NONE
PRESTWOOD	v		41	PRESTWOOD04RR01P01	J008	04RR01P02J008	PRESTWOOD04RR01	NONE
PRESTWOOD	v		41	PRESTWOOD04RR01P01	J009	04RR01P02J009	PRESTWOOD04RR01	NONE
PRESTWOOD	v		41	PRESTWOOD04RR01P01	J010	04RR01P02J010	PRESTWOOD04RR01	NONE
PRESTWOOD	v		41	PRESTWOOD04RR01P01	J011	04RR01P02J011	PRESTWOOD04RR01	NONE
PRESTWOOD	v		42	PRESTWOOD04RR01P01	J012	04RR01P02J012	PRESTWOOD04RR01	NONE
PRESTWOOD	v		42	PRESTWOOD04RR01P01	J013	04RR01P02J013	PRESTWOOD04RR01	NONE
PRESTWOOD	v		42	PRESTWOOD04RR01P01	J014	04RR01P02J014	PRESTWOOD04RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J001	01RR01P02J001	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J002	01RR01P02J002	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J003	01RR01P02J003	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J004	01RR01P02J004	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J005	01RR01P02J005	PRESTWOOD01RR01	NONE



<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J006	01RR01P02J006	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J007	01RR01P02J007	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J008	01RR01P02J008	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J009	01RR01P02J009	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J010	01RR01P02J010	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J011	01RR01P02J011	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J012	01RR01P02J012	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J013	01RR01P02J013	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J014	01RR01P02J014	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J015	01RR01P02J015	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J016	01RR01P02J016	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J017	01RR01P02J017	PRESTWOOD01RR01	NONE
PRESTWOOD	v		C.LAB	PRESTWOOD01RR01P01	J018	01RR01P02J018	PRESTWOOD01RR01	NONE
PRESTWOOD	v		LIBRARY	PRESTWOOD01RR01P01	J019	01RR01P02J019	PRESTWOOD01RR01	NONE
PRESTWOOD	v		LIBRARY	PRESTWOOD01RR01P01	J020	01RR01P02J020	PRESTWOOD01RR01	NONE
PRESTWOOD	v		LIBRARY	PRESTWOOD01RR01P01	J021	01RR01P02J021	PRESTWOOD01RR01	NONE
PRESTWOOD	v		LIBRARY	PRESTWOOD01RR01P01	J022	01RR01P02J022	PRESTWOOD01RR01	NONE
PRESTWOOD	v		LIBRARY	PRESTWOOD01RR01P01	J023	01RR01P02J023	PRESTWOOD01RR01	NONE
PRESTWOOD	v		LIBRARY	PRESTWOOD01RR01P01	J024	01RR01P02J024	PRESTWOOD01RR01	NONE
PRESTWOOD	v		COPIER RM	PRESTWOOD01RR01P01	J025	01RR01P02J025	PRESTWOOD01RR01	NONE
PRESTWOOD	v		COPIER RM	PRESTWOOD01RR01P01	J026	01RR01P02J026	PRESTWOOD01RR01	NONE
PRESTWOOD	v		COPIER RM	PRESTWOOD01RR01P01	J027	01RR01P02J027	PRESTWOOD01RR01	NONE
PRESTWOOD	v		STAFF LOUNGE	PRESTWOOD01RR01P01	J028	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		STAFF LOUNGE	PRESTWOOD01RR01P01	J029	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		STAFF LOUNGE	PRESTWOOD01RR01P01	J030	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		OFFICE	PRESTWOOD01RR01P01	J031	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		OFFICE	PRESTWOOD01RR01P01	J032	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		OFFICE	PRESTWOOD01RR01P01	J033	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		OFFICE	PRESTWOOD01RR01P01	J034	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		OFFICE	PRESTWOOD01RR01P01	J035	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		OFFICE	PRESTWOOD01RR01P01	J036	01RR01P02J028	PRESTWOOD01RR01	NONE
PRESTWOOD	v		50	PRESTWOOD05RR01P01	J001	05RR01P02J001	PRESTWOOD05RR01	NONE
PRESTWOOD	v		50	PRESTWOOD05RR01P01	J002	05RR01P02J002	PRESTWOOD05RR01	NONE
PRESTWOOD	v		50	PRESTWOOD05RR01P01	J003	05RR01P02J003	PRESTWOOD05RR01	NONE
PRESTWOOD	v		50	PRESTWOOD05RR01P01	J004	05RR01P02J004	PRESTWOOD05RR01	NONE
PRESTWOOD	v		51	PRESTWOOD05RR01P01	J005	05RR01P02J005	PRESTWOOD05RR01	NONE
PRESTWOOD	v		51	PRESTWOOD05RR01P01	J006	05RR01P02J006	PRESTWOOD05RR01	NONE
PRESTWOOD	v		51	PRESTWOOD05RR01P01	J007	05RR01P02J007	PRESTWOOD05RR01	NONE
PRESTWOOD	v		51	PRESTWOOD05RR01P01	J008	05RR01P02J008	PRESTWOOD05RR01	NONE
PRESTWOOD	v		52	PRESTWOOD05RR01P01	J009	05RR01P02J009	PRESTWOOD05RR01	NONE
PRESTWOOD	v		52	PRESTWOOD05RR01P01	J010	05RR01P02J010	PRESTWOOD05RR01	NONE
PRESTWOOD	v		52	PRESTWOOD05RR01P01	J011	05RR01P02J011	PRESTWOOD05RR01	NONE
PRESTWOOD	v		52	PRESTWOOD05RR01P01	J012	05RR01P02J012	PRESTWOOD05RR01	NONE







**SASSARINI ELEMENTARY SCHOOL**  
652 Fifth Street West, Sonoma, CA 95476

REVIEWED	APPROVED
BENEDICT SIGUA	ARNOLD DAMIAN, RCDD
STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SASSARINI	v		4.1	SASSARINI01RR01P01	J001	01RR01P01J001	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J002	01RR01P01J002	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J003	01RR01P01J003	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J004	01RR01P01J004	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J005	01RR01P01J005	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J006	01RR01P01J006	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J007	01RR01P01J007	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J008	01RR01P01J008	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J009	01RR01P01J009	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J010	01RR01P01J010	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J011	01RR01P01J011	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J012	01RR01P01J012	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J013	01RR01P01J013	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J014	01RR01P01J014	SASSARINI01RR01	NONE
SASSARINI	v		4.1	SASSARINI01RR01P01	J015	01RR01P01J015	SASSARINI01RR01	NOT LOCATED
SASSARINI	v		4.1	SASSARINI01RR01P01	J016	01RR01P01J016	SASSARINI01RR01	NOT LOCATED
SASSARINI	v		4.1	SASSARINI01RR01P01	J017	01RR01P01J017	SASSARINI01RR01	NOT LOCATED
SASSARINI	v		4.1	SASSARINI01RR01P01	J018	01RR01P01J018	SASSARINI01RR01	NOT LOCATED
SASSARINI	v		4.2	SASSARINI01RR01P01	J019	01RR01P01J019	SASSARINI01RR01	NONE
SASSARINI	v		4.2	SASSARINI01RR01P01	J020	01RR01P01J020	SASSARINI01RR01	NONE
SASSARINI	v		4.2	SASSARINI01RR01P01	J021	01RR01P01J021	SASSARINI01RR01	NONE
SASSARINI	v		4.2	SASSARINI01RR01P01	J022	01RR01P01J022	SASSARINI01RR01	NONE
SASSARINI	v		4.2	SASSARINI01RR01P01	J023	01RR01P01J023	SASSARINI01RR01	NONE
SASSARINI	v		4.2	SASSARINI01RR01P01	J024	01RR01P01J024	SASSARINI01RR01	NONE
SASSARINI	v		4.2	SASSARINI01RR01P01	J025	01RR01P02J025	SASSARINI01RR01	NONE
SASSARINI	v		4.2	SASSARINI01RR01P01	J026	01RR01P02J026	SASSARINI01RR01	NONE
SASSARINI	v		STAFF RM.	SASSARINI01RR01P01	J027	01RR01P02J027	SASSARINI01RR01	NONE
SASSARINI	v		STAFF RM.	SASSARINI01RR01P01	J028	01RR01P02J028	SASSARINI01RR01	NONE
SASSARINI	v		STAFF RM.	SASSARINI01RR01P01	J029	01RR01P02J029	SASSARINI01RR01	NONE
SASSARINI	v		STAFF RM.	SASSARINI01RR01P01	J030	01RR01P02J030	SASSARINI01RR01	NONE
SASSARINI	v		5.1	SASSARINI03RR01P01	J001	03RR01P01J001	SASSARINI03RR01	NONE
SASSARINI	v		5.1	SASSARINI03RR01P01	J002	03RR01P01J002	SASSARINI03RR01	NONE
SASSARINI	v		5.1	SASSARINI03RR01P01	J003	03RR01P01J003	SASSARINI03RR01	NONE
SASSARINI	v		5.1	SASSARINI03RR01P01	J004	03RR01P01J004	SASSARINI03RR01	NONE
SASSARINI	v		5.2	SASSARINI03RR01P01	J005	03RR01P01J005	SASSARINI03RR01	NOT LOCATED
SASSARINI	v		5.2	SASSARINI03RR01P01	J006	03RR01P01J006	SASSARINI03RR01	NOT LOCATED
SASSARINI	v		5.3	SASSARINI03RR01P01	J007	03RR01P01J007	SASSARINI03RR01	NONE
SASSARINI	v		5.3	SASSARINI03RR01P01	J008	03RR01P01J008	SASSARINI03RR01	NONE

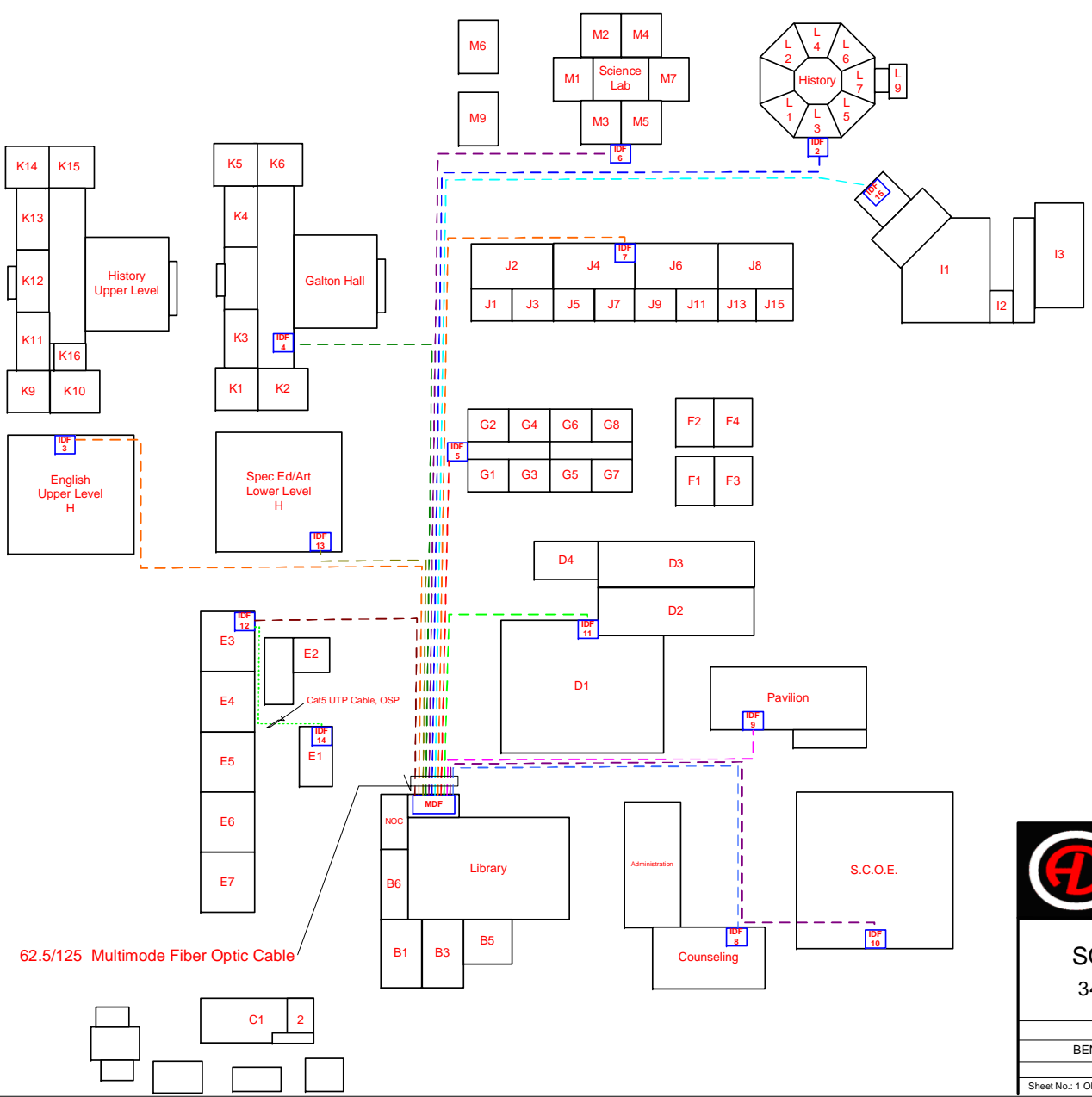
<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SASSARINI	v		5.3	SASSARINI03RR01P01	J009	03RR01P01J009	SASSARINI03RR01	NONE
SASSARINI	v		5.3	SASSARINI03RR01P01	J010	03RR01P01J010	SASSARINI03RR01	NONE
SASSARINI	v		5.3	SASSARINI03RR01P01	J011	03RR01P01J011	SASSARINI03RR01	NONE
SASSARINI	v		5.3	SASSARINI03RR01P01	J012	03RR01P01J012	SASSARINI03RR01	NONE
SASSARINI	v		6.1	SASSARINI02RR01P01	J001	02RR01P01J001	SASSARINI02RR01	NONE
SASSARINI	v		6.1	SASSARINI02RR01P01	J002	02RR01P01J002	SASSARINI02RR01	NONE
SASSARINI	v		6.1	SASSARINI02RR01P01	J003	02RR01P01J003	SASSARINI02RR01	NONE
SASSARINI	v		6.1	SASSARINI02RR01P01	J004	02RR01P01J004	SASSARINI02RR01	NONE
SASSARINI	v		6.2	SASSARINI02RR01P01	J005	02RR01P01J005	SASSARINI02RR01	NONE
SASSARINI	v		6.2	SASSARINI02RR01P01	J006	02RR01P01J006	SASSARINI02RR01	NONE
SASSARINI	v		6.2	SASSARINI02RR01P01	J007	02RR01P01J007	SASSARINI02RR01	NONE
SASSARINI	v		6.2	SASSARINI02RR01P01	J008	02RR01P01J008	SASSARINI02RR01	NONE
SASSARINI	v		6.3	SASSARINI02RR01P01	J009	02RR01P01J009	SASSARINI02RR01	NONE
SASSARINI	v		6.3	SASSARINI02RR01P01	J010	02RR01P01J010	SASSARINI02RR01	NONE
SASSARINI	v		6.3	SASSARINI02RR01P01	J011	02RR01P01J011	SASSARINI02RR01	NONE
SASSARINI	v		6.3	SASSARINI02RR01P01	J012	02RR01P01J012	SASSARINI02RR01	NONE
SASSARINI	v		6.4	SASSARINI02RR01P01	J013	02RR01P01J013	SASSARINI02RR01	NONE
SASSARINI	v		6.4	SASSARINI02RR01P01	J014	02RR01P01J014	SASSARINI02RR01	NONE
SASSARINI	v		6.4	SASSARINI02RR01P01	J015	02RR01P01J015	SASSARINI02RR01	NONE
SASSARINI	v		6.4	SASSARINI02RR01P01	J016	02RR01P01J016	SASSARINI02RR01	NONE
SASSARINI	v		6.5	SASSARINI02RR01P01	J017	02RR01P01J017	SASSARINI02RR01	NONE
SASSARINI	v		6.5	SASSARINI02RR01P01	J018	02RR01P01J018	SASSARINI02RR01	NONE
SASSARINI	v		6.5	SASSARINI02RR01P01	J019	02RR01P01J019	SASSARINI02RR01	NONE
SASSARINI	v		6.5	SASSARINI02RR01P01	J020	02RR01P01J020	SASSARINI02RR01	NONE
SASSARINI	v		6.6	SASSARINI02RR01P01	J021	02RR01P01J021	SASSARINI02RR01	NONE
SASSARINI	v		6.6	SASSARINI02RR01P01	J022	02RR01P01J022	SASSARINI02RR01	NONE
SASSARINI	v		6.6	SASSARINI02RR01P01	J023	02RR01P01J023	SASSARINI02RR01	NONE
SASSARINI	v		6.6	SASSARINI02RR01P01	J024	02RR01P01J024	SASSARINI02RR01	NONE
SASSARINI	v		6.7	SASSARINI02RR01P01	J025	02RR01P02J025	SASSARINI02RR01	NONE
SASSARINI	v		6.7	SASSARINI02RR01P01	J026	02RR01P02J026	SASSARINI02RR01	NOT LOCATED
SASSARINI	v		6.7	SASSARINI02RR01P01	J027	02RR01P02J027	SASSARINI02RR01	NONE
SASSARINI	v		6.7	SASSARINI02RR01P01	J028	02RR01P02J028	SASSARINI02RR01	NONE
SASSARINI	v		6.8	SASSARINI02RR01P01	J029	02RR01P02J029	SASSARINI02RR01	NONE
SASSARINI	v		6.8	SASSARINI02RR01P01	J030	02RR01P02J030	SASSARINI02RR01	NOT LOCATED
SASSARINI	v		6.8	SASSARINI02RR01P01	J031	02RR01P02J029	SASSARINI02RR01	NOT LOCATED
SASSARINI	v		6.8	SASSARINI02RR01P01	J032	02RR01P02J030	SASSARINI02RR01	NONE
SASSARINI	v		2.1	SASSARINI07RR01P01	J001	07RR01P01J001	SASSARINI07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SASSARINI	v		2.1	SASSARINI07RR01P01	J002	07RR01P01J002	SASSARINI07RR01	NONE
SASSARINI	v		2.1	SASSARINI07RR01P01	J003	07RR01P01J003	SASSARINI07RR01	NONE
SASSARINI	v		2.1	SASSARINI07RR01P01	J004	07RR01P01J004	SASSARINI07RR01	NONE
SASSARINI	v		2.2	SASSARINI07RR01P01	J005	07RR01P01J005	SASSARINI07RR01	NONE
SASSARINI	v		2.2	SASSARINI07RR01P01	J006	07RR01P01J006	SASSARINI07RR01	NONE
SASSARINI	v		2.3	SASSARINI07RR01P01	J007	07RR01P01J007	SASSARINI07RR01	NONE
SASSARINI	v		2.3	SASSARINI07RR01P01	J008	07RR01P01J008	SASSARINI07RR01	NONE
SASSARINI	v		2.3	SASSARINI07RR01P01	J009	07RR01P01J009	SASSARINI07RR01	NONE
SASSARINI	v		2.3	SASSARINI07RR01P01	J010	07RR01P01J010	SASSARINI07RR01	NONE
SASSARINI	v		2.3	SASSARINI07RR01P01	J011	07RR01P01J011	SASSARINI07RR01	NONE
SASSARINI	v		2.3	SASSARINI07RR01P01	J012	07RR01P01J012	SASSARINI07RR01	NONE
SASSARINI	v		1.1	SASSARINI08RR01P01	J001	08RR01P01J001	SASSARINI08RR01	NONE
SASSARINI	v		1.1	SASSARINI08RR01P01	J002	08RR01P01J002	SASSARINI08RR01	NONE
SASSARINI	v		1.1	SASSARINI08RR01P01	J003	08RR01P01J003	SASSARINI08RR01	NONE
SASSARINI	v		1.1	SASSARINI08RR01P01	J004	08RR01P01J004	SASSARINI08RR01	NONE
SASSARINI	v		1.1	SASSARINI08RR01P01	J005	08RR01P01J005	SASSARINI08RR01	NONE
SASSARINI	v		1.1	SASSARINI08RR01P01	J006	08RR01P01J006	SASSARINI08RR01	NONE
SASSARINI	v		1.2	SASSARINI08RR01P01	J007	08RR01P01J007	SASSARINI08RR01	NONE
SASSARINI	v		1.2	SASSARINI08RR01P01	J008	08RR01P01J008	SASSARINI08RR01	NONE
SASSARINI	v		1.2	SASSARINI08RR01P01	J009	08RR01P01J009	SASSARINI08RR01	NONE
SASSARINI	v		1.2	SASSARINI08RR01P01	J010	08RR01P01J010	SASSARINI08RR01	NONE
SASSARINI	v		1.2	SASSARINI08RR01P01	J011	08RR01P01J011	SASSARINI08RR01	NONE
SASSARINI	v		1.2	SASSARINI08RR01P01	J012	08RR01P01J012	SASSARINI08RR01	NONE
SASSARINI	v		7.7	SASSARINI05RR01P01	J001	05RR01P01J001	SASSARINI05RR01	NONE
SASSARINI	v		7.7	SASSARINI05RR01P01	J002	05RR01P01J002	SASSARINI05RR01	NONE
SASSARINI	v		7.7	SASSARINI05RR01P01	J003	05RR01P01J003	SASSARINI05RR01	NONE
SASSARINI	v		7.7	SASSARINI05RR01P01	J004	07RR01P01J004	SASSARINI05RR01	NONE
SASSARINI	v		7.1	SASSARINI09RR01P01	J001	09RR01P01J005	SASSARINI09RR01	NONE
SASSARINI	v		7.1	SASSARINI09RR01P01	J002	09RR01P01J006	SASSARINI09RR01	NONE
SASSARINI	v		7.1	SASSARINI09RR01P01	J003	09RR01P01J007	SASSARINI09RR01	NONE
SASSARINI	v		7.1	SASSARINI09RR01P01	J004	09RR01P01J008	SASSARINI09RR01	NONE
SASSARINI	v		7.2	SASSARINI09RR01P01	J009	09RR01P01J009	SASSARINI09RR01	NONE
SASSARINI	v		7.2	SASSARINI09RR01P01	J010	09RR01P01J010	SASSARINI09RR01	NONE
SASSARINI	v		7.3	SASSARINI04RR01P01	J001	04RR01P01J001	SASSARINI04RR01	NONE
SASSARINI	v		7.3	SASSARINI04RR01P01	J002	04RR01P01J002	SASSARINI04RR01	NONE
SASSARINI	v		7.3	SASSARINI04RR01P01	J003	04RR01P01J003	SASSARINI04RR01	NONE





<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J001	06RR01P01J001	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J002	06RR01P01J002	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J003	06RR01P01J003	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J004	06RR01P01J004	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J005	06RR01P01J005	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J006	06RR01P01J006	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J007	06RR01P01J007	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J008	06RR01P01J008	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J009	06RR01P01J009	SASSARINI06RR01	NOT LOCATED
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J010	06RR01P01J010	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J011	06RR01P01J011	SASSARINI06RR01	NONE
SASSARINI	v		ADMIN	SASSARINI06RR01P01	J012	06RR01P01J012	SASSARINI06RR01	NONE



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BENEDICT SIGUA	ARNOLD DAMIAN, RCDD
STRUCTURED CABLING INFRASTRUCTURE	
Sheet No.: 1 OF 1	DATE : 21 NOVEMBER 2008

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		K15	SVHS04RR01P01	J001	04RR01P01J001	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J002	04RR01P01J002	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J003	04RR01P01J003	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J004	04RR01P01J004	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J005	04RR01P01J005	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J006	04RR01P01J006	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J007	04RR01P01J007	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J008	04RR01P01J008	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J009	04RR01P01J009	SVHS04RR01	NONE
SVHS	v		K15	SVHS04RR01P01	J010	04RR01P01J010	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J011	04RR01P01J011	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J012	04RR01P01J012	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J013	04RR01P01J013	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J014	04RR01P01J014	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J015	04RR01P01J015	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J016	04RR01P01J016	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J017	04RR01P01J017	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J018	04RR01P01J018	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J019	04RR01P01J019	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J020	04RR01P01J020	SVHS04RR01	NONE
SVHS	v		K14	SVHS04RR01P01	J021	04RR01P01J021	SVHS04RR01	NONE
SVHS	v		K13	SVHS04RR01P01	J022	04RR01P01J022	SVHS04RR01	NONE
SVHS	v		K13	SVHS04RR01P01	J023	04RR01P01J023	SVHS04RR01	NONE
SVHS	v		K13	SVHS04RR01P01	J024	04RR01P01J024	SVHS04RR01	NONE
SVHS	v		K13	SVHS04RR01P01	J025	04RR01P02J025	SVHS04RR01	NONE
SVHS	v		K13	SVHS04RR01P01	J026	04RR01P02J026	SVHS04RR01	NONE
SVHS	v		K13	SVHS04RR01P01	J027	04RR01P02J027	SVHS04RR01	NONE
SVHS	v		K13	SVHS04RR01P01	J028	04RR01P02J028	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J029	04RR01P02J029	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J030	04RR01P02J030	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J031	04RR01P02J031	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J032	04RR01P02J032	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J033	04RR01P02J033	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J034	04RR01P02J034	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J035	04RR01P02J035	SVHS04RR01	NONE
SVHS	v		K12	SVHS04RR01P01	J036	04RR01P02J036	SVHS04RR01	NONE
SVHS	v		K11	SVHS04RR01P01	J037	04RR01P02J037	SVHS04RR01	NONE
SVHS	v		K11	SVHS04RR01P01	J038	04RR01P02J038	SVHS04RR01	NONE
SVHS	v		K11	SVHS04RR01P01	J039	04RR01P02J039	SVHS04RR01	NONE
SVHS	v		K11	SVHS04RR01P01	J040	04RR01P02J040	SVHS04RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		K11	SVHS04RR01P01	J041	04RR01P01J039	SVHS04RR01	NONE
SVHS	v		K11	SVHS04RR01P01	J042	04RR01P01J041	SVHS04RR01	NONE
SVHS	v		K11	SVHS04RR01P01	J043	04RR01P01J042	SVHS04RR01	NONE
SVHS	v		K11	SVHS04RR01P01	J044	04RR01P01J043	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J045	04RR01P01J044	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J046	04RR01P01J045	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J047	04RR01P01J046	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J048	04RR01P01J047	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J049	04RR01P01J048	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J050	04RR01P01J049	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J051	04RR01P01J051	SVHS04RR01	NONE
SVHS	v		K9	SVHS04RR01P01	J052	04RR01P01J052	SVHS04RR01	NONE
SVHS	v		K10	SVHS04RR01P01	J053	04RR01P01J053	SVHS04RR01	NONE
SVHS	v		K10	SVHS04RR01P01	J054	04RR01P01J054	SVHS04RR01	NONE
SVHS	v		K10	SVHS04RR01P01	J055	04RR01P01J055	SVHS04RR01	NONE
SVHS	v		K10	SVHS04RR01P01	J056	04RR01P01J056	SVHS04RR01	NONE
SVHS	v		K10	SVHS04RR01P01	J057	04RR01P01J057	SVHS04RR01	NONE
SVHS			K10	SVHS04RR01P01	J058	04RR01P01J058	SVHS04RR01	NONE
SVHS	v		K10	SVHS04RR01P01	J059	04RR01P01J059	SVHS04RR01	NONE
SVHS	v		K10	SVHS04RR01P01	J060	04RR01P01J060	SVHS04RR01	NONE
SVHS	v		K16	SVHS04RR01P01	J061	04RR01P01J061	SVHS04RR01	NONE
SVHS	v		K16	SVHS04RR01P01	J062	04RR01P01J062	SVHS04RR01	NONE
SVHS	v		K16	SVHS04RR01P01	J063	04RR01P01J063	SVHS04RR01	NONE
SVHS	v		K16	SVHS04RR01P01	J064	04RR01P01J064	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J001	04RR01P01J001	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J002	04RR01P01J002	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J003	04RR01P01J003	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J004	04RR01P01J004	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J005	04RR01P01J005	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J006	04RR01P01J006	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J007	04RR01P01J007	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J008	04RR01P01J008	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J009	04RR01P01J009	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J010	04RR01P01J010	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J011	04RR01P01J011	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J012	04RR01P01J012	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J013	04RR01P01J013	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J014	04RR01P01J014	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J015	04RR01P01J015	SVHS04RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		K6	SVHS04RR01P01	J016	04RR01P01J016	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J017	04RR01P01J017	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J018	04RR01P01J018	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J019	04RR01P01J019	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J020	04RR01P01J020	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J021	04RR01P01J021	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J022	04RR01P01J022	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J023	04RR01P01J023	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J024	04RR01P01J024	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J025	04RR01P02J025	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J026	04RR01P02J026	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J027	04RR01P02J027	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J028	04RR01P02J028	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J029	04RR01P02J029	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J030	04RR01P02J030	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J031	04RR01P02J031	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J032	04RR01P02J032	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J033	04RR01P02J033	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J034	04RR01P02J034	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J035	04RR01P02J035	SVHS04RR01	NONE
SVHS	v		K6	SVHS04RR01P01	J036	04RR01P02J036	SVHS04RR01	NONE
SVHS	v		K COPY RM	SVHS04RR01P01	J037	04RR01P02J037	SVHS04RR01	NONE
SVHS	v		K COPY RM	SVHS04RR01P01	J038	04RR01P02J038	SVHS04RR01	NONE
SVHS	v		K COPY RM	SVHS04RR01P01	J039	04RR01P02J039	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J040	04RR01P02J040	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J041	04RR01P01J039	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J042	04RR01P01J041	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J043	04RR01P01J042	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J044	04RR01P01J043	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J045	04RR01P01J044	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J046	04RR01P01J045	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J047	04RR01P01J046	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J048	04RR01P01J047	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J049	04RR01P01J048	SVHS04RR01	NONE
SVHS	v		K5	SVHS04RR01P01	J050	04RR01P01J049	SVHS04RR01	NONE
SVHS	v		K4	SVHS04RR01P01	J051	04RR01P01J051	SVHS04RR01	NONE
SVHS	v		K4	SVHS04RR01P01	J052	04RR01P01J052	SVHS04RR01	NONE
SVHS	v		K4	SVHS04RR01P01	J053	04RR01P01J053	SVHS04RR01	NONE
SVHS	v		K4	SVHS04RR01P01	J054	04RR01P01J054	SVHS04RR01	NONE
SVHS	v		K4	SVHS04RR01P01	J055	04RR01P01J055	SVHS04RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		K4	SVHS04RR01P01	J056	04RR01P01J056	SVHS04RR01	NONE
SVHS	v		K4	SVHS04RR01P01	J057	04RR01P01J057	SVHS04RR01	NONE
SVHS	v		K4	SVHS04RR01P01	J058	04RR01P01J058	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J059	04RR01P01J059	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J060	04RR01P01J060	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J061	04RR01P01J061	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J062	04RR01P01J062	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J063	04RR01P01J063	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J064	04RR01P01J064	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J065	04RR01P01J065	SVHS04RR01	NONE
SVHS	v		K3	SVHS04RR01P01	J066	04RR01P01J066	SVHS04RR01	NONE
SVHS	v		K1	SVHS04RR01P01	J067	04RR01P01J067	SVHS04RR01	NONE
SVHS	v		K1	SVHS04RR01P01	J068	04RR01P01J068	SVHS04RR01	NONE
SVHS	v		K1	SVHS04RR01P01	J069	04RR01P01J069	SVHS04RR01	NONE
SVHS	v		K1	SVHS04RR01P01	J070	04RR01P01J070	SVHS04RR01	NONE
SVHS	v		K1	SVHS04RR01P01	J071	04RR01P01J071	SVHS04RR01	NONE
SVHS	v		K1	SVHS04RR01P01	J072	04RR01P01J072	SVHS04RR01	NONE
SVHS	v		K1	SVHS04RR01P01	J073	04RR01P01J073	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J074	04RR01P01J074	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J075	04RR01P01J075	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J076	04RR01P01J076	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J077	04RR01P01J077	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J078	04RR01P01J078	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J079	04RR01P01J079	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J080	04RR01P01J080	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J081	04RR01P01J081	SVHS04RR01	NONE
SVHS	v		K2	SVHS04RR01P01	J082	04RR01P01J082	SVHS04RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J001	01RR01P01J001	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J002	01RR01P01J002	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J003	01RR01P01J003	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J004	01RR01P01J004	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J005	01RR01P01J005	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J006	01RR01P01J006	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J007	01RR01P01J007	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J008	01RR01P01J008	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J009	01RR01P01J009	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J010	01RR01P01J010	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J011	01RR01P01J011	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J012	01RR01P01J012	SVHS01RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		B1-B3	SVHS01RR01P01	J013	01RR01P01J013	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J014	01RR01P01J014	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J015	01RR01P01J015	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J016	01RR01P01J016	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J017	01RR01P01J017	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J018	01RR01P01J018	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J019	01RR01P01J019	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J020	01RR01P01J020	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J021	01RR01P01J021	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J022	01RR01P01J022	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J023	01RR01P01J023	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J024	01RR01P01J024	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J025	01RR01P02J025	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J026	01RR01P02J026	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J027	01RR01P02J027	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J028	01RR01P02J028	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J029	01RR01P02J029	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J030	01RR01P02J030	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J031	01RR01P02J031	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J032	01RR01P02J032	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J033	01RR01P02J033	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J034	01RR01P02J034	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J035	01RR01P02J035	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J036	01RR01P02J036	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J037	01RR01P02J037	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J038	01RR01P02J038	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J039	01RR01P02J039	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J040	01RR01P02J040	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J041	01RR01P01J039	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J042	01RR01P01J041	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J043	01RR01P01J042	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J044	01RR01P01J043	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J045	01RR01P01J044	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J046	01RR01P01J045	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J047	01RR01P01J046	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J048	01RR01P01J047	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J049	01RR01P01J048	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J050	01RR01P01J049	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J051	01RR01P01J051	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J052	01RR01P01J052	SVHS01RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		B1-B3	SVHS01RR01P01	J053	01RR01P01J053	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J054	01RR01P01J054	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J055	01RR01P01J055	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J056	01RR01P01J056	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J057	01RR01P01J057	SVHS01RR01	NONE
SVHS	v		B1-B3	SVHS01RR01P01	J058	01RR01P01J058	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J059	01RR01P01J059	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J060	01RR01P01J060	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J061	01RR01P01J061	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J062	01RR01P01J062	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J063	01RR01P01J063	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J064	01RR01P01J064	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J065	01RR01P01J065	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J066	01RR01P01J066	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J067	01RR01P01J067	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J068	01RR01P01J068	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J069	01RR01P01J069	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J070	01RR01P01J070	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J071	01RR01P01J071	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J072	01RR01P01J072	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J073	01RR01P01J073	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J074	01RR01P01J074	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J075	01RR01P01J075	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J076	01RR01P01J076	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J077	01RR01P01J077	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J078	01RR01P01J078	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J079	01RR01P01J079	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J080	01RR01P01J080	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J081	01RR01P01J081	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J082	01RR01P01J082	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J083	01RR01P01J083	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J084	01RR01P01J084	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J085	01RR01P01J085	SVHS01RR01	NONE
SVHS	v		LIBRARY	SVHS01RR01P01	J086	01RR01P01J086	SVHS01RR01	NONE
SVHS	v		B4	SVHS01RR01P01	J087	01RR01P01J087	SVHS01RR01	NONE
SVHS	v		B4	SVHS01RR01P01	J088	01RR01P01J088	SVHS01RR01	NONE
SVHS	v		B4	SVHS01RR01P01	J089	01RR01P01J089	SVHS01RR01	NONE
SVHS	v		B4	SVHS01RR01P01	J090	01RR01P01J090	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J091	01RR01P01J091	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J092	01RR01P01J092	SVHS01RR01	NONE



<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		NOC	SVHS01RR01P01	J093	01RR01P01J093	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J094	01RR01P01J094	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J095	01RR01P01J095	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J096	01RR01P01J096	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J097	01RR01P01J097	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J098	01RR01P01J098	SVHS01RR01	NONE
SVHS	v		NOC	SVHS01RR01P01	J099	01RR01P01J099	SVHS01RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J001	07RR01P01J001	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J002	07RR01P01J002	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J003	07RR01P01J003	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J004	07RR01P01J004	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J005	07RR01P01J005	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J006	07RR01P01J006	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J007	07RR01P01J007	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J008	07RR01P01J008	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J009	07RR01P01J009	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J010	07RR01P01J010	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J011	07RR01P01J011	SVHS07RR01	NONE
SVHS	v		J15	SVHS07RR01P01	J012	07RR01P01J012	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J013	07RR01P01J013	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J014	07RR01P01J014	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J015	07RR01P01J015	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J016	07RR01P01J016	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J017	07RR01P01J017	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J018	07RR01P01J018	SVHS07RR01	NONE
SVHS	v		J2	SVHS07RR01P01	J019	07RR01P01J019	SVHS07RR01	NONE
SVHS	v		J2	SVHS07RR01P01	J020	07RR01P01J020	SVHS07RR01	NONE
SVHS	v		J2	SVHS07RR01P01	J021	07RR01P01J021	SVHS07RR01	NONE
SVHS	v		J2	SVHS07RR01P01	J022	07RR01P01J022	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J023	07RR01P01J023	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J024	07RR01P01J024	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J025	07RR01P01J025	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J026	07RR01P01J026	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J027	07RR01P01J027	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J028	07RR01P01J028	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J029	07RR01P01J029	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J030	07RR01P01J030	SVHS07RR01	NONE
SVHS	v		J13	SVHS07RR01P01	J031	07RR01P01J031	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J032	07RR01P01J032	SVHS07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		J11	SVHS07RR01P01	J033	07RR01P01J033	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J034	07RR01P01J034	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J035	07RR01P01J035	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J036	07RR01P01J036	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J037	07RR01P01J037	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J038	07RR01P01J038	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J039	07RR01P01J039	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J040	07RR01P01J040	SVHS07RR01	NONE
SVHS	v		J11	SVHS07RR01P01	J041	07RR01P01J041	SVHS07RR01	NONE
SVHS	v		J9	SVHS07RR01P01	J042	07RR01P01J042	SVHS07RR01	NONE
SVHS	v		J9	SVHS07RR01P01	J043	07RR01P01J043	SVHS07RR01	NONE
SVHS	v		J9	SVHS07RR01P01	J044	07RR01P01J044	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J045	07RR01P01J045	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J046	07RR01P01J046	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J047	07RR01P01J047	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J048	07RR01P01J048	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J049	07RR01P01J049	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J050	07RR01P01J050	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J051	07RR01P01J051	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J052	07RR01P01J052	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J053	07RR01P01J053	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J054	07RR01P01J054	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J055	07RR01P01J055	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J056	07RR01P01J056	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J057	07RR01P01J057	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J058	07RR01P01J058	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J059	07RR01P01J059	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J060	07RR01P01J060	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J061	07RR01P01J061	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J062	07RR01P01J062	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J063	07RR01P01J063	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J064	07RR01P01J064	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J065	07RR01P01J065	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J066	07RR01P01J066	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J067	07RR01P01J067	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J068	07RR01P01J068	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J069	07RR01P01J069	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J070	07RR01P01J070	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J071	07RR01P01J071	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J072	07RR01P01J072	SVHS07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		J8	SVHS07RR01P01	J073	07RR01P01J073	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J074	07RR01P01J074	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J075	07RR01P01J075	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J076	07RR01P01J076	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J077	07RR01P01J077	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J078	07RR01P01J078	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J079	07RR01P01J079	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J080	07RR01P01J080	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J081	07RR01P01J081	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J082	07RR01P01J082	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J083	07RR01P01J083	SVHS07RR01	NONE
SVHS	v		J8	SVHS07RR01P01	J084	07RR01P01J084	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J085	07RR01P01J085	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J086	07RR01P01J086	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J087	07RR01P01J087	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J088	07RR01P01J088	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J089	07RR01P01J089	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J090	07RR01P01J090	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J091	07RR01P01J091	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J092	07RR01P01J092	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J093	07RR01P01J093	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J094	07RR01P01J094	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J095	07RR01P01J095	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J096	07RR01P01J096	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J097	07RR01P01J097	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J098	07RR01P01J098	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J099	07RR01P01J099	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J100	07RR01P01J100	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J101	07RR01P01J101	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J102	07RR01P01J102	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J103	07RR01P01J103	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J104	07RR01P01J104	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J105	07RR01P01J105	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J106	07RR01P01J106	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J107	07RR01P01J107	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J108	07RR01P01J108	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J109	07RR01P01J109	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J110	07RR01P01J110	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J111	07RR01P01J111	SVHS07RR01	NONE
SVHS	v		J7	SVHS07RR01P01	J112	07RR01P01J112	SVHS07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		J7	SVHS07RR01P01	J113	07RR01P01J113	SVHS07RR01	NONE
SVHS	v		J7	SVHS07RR01P01	J114	07RR01P01J114	SVHS07RR01	NONE
SVHS	v		J7	SVHS07RR01P01	J115	07RR01P01J115	SVHS07RR01	NONE
SVHS	v		J7	SVHS07RR01P01	J116	07RR01P01J116	SVHS07RR01	NONE
SVHS	v		J7	SVHS07RR01P01	J117	07RR01P01J117	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J118	07RR01P01J118	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J119	07RR01P01J119	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J120	07RR01P01J120	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J121	07RR01P01J121	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J122	07RR01P01J122	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J123	07RR01P01J123	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J124	07RR01P01J124	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J125	07RR01P01J125	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J126	07RR01P01J126	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J127	07RR01P01J127	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J128	07RR01P01J128	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J129	07RR01P01J129	SVHS07RR01	NONE
SVHS	v		J6	SVHS07RR01P01	J130	07RR01P01J130	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J131	07RR01P01J131	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J132	07RR01P01J132	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J133	07RR01P01J133	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J134	07RR01P01J134	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J135	07RR01P01J135	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J136	07RR01P01J136	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J137	07RR01P01J137	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J138	07RR01P01J138	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J139	07RR01P01J139	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J140	07RR01P01J140	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J141	07RR01P01J141	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J142	07RR01P01J142	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J143	07RR01P01J143	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J144	07RR01P01J144	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J145	07RR01P01J145	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J146	07RR01P01J146	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J147	07RR01P01J147	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J148	07RR01P01J148	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J149	07RR01P01J149	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J150	07RR01P01J150	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J151	07RR01P01J151	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J152	07RR01P01J152	SVHS07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		J4	SVHS07RR01P01	J153	07RR01P01J153	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J154	07RR01P01J154	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J155	07RR01P01J155	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J156	07RR01P01J156	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J157	07RR01P01J157	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J158	07RR01P01J158	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J159	07RR01P01J159	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J160	07RR01P01J160	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J161	07RR01P01J161	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J162	07RR01P01J162	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J163	07RR01P01J163	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J164	07RR01P01J164	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J165	07RR01P01J165	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J166	07RR01P01J166	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J167	07RR01P01J167	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J168	07RR01P01J168	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J169	07RR01P01J169	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J170	07RR01P01J170	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J171	07RR01P01J171	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J172	07RR01P01J172	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J173	07RR01P01J173	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J174	07RR01P01J174	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J175	07RR01P01J175	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J176	07RR01P01J176	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J177	07RR01P01J177	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J178	07RR01P01J178	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J179	07RR01P01J179	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J180	07RR01P01J180	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J181	07RR01P01J181	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J182	07RR01P01J182	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J183	07RR01P01J183	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J184	07RR01P01J184	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J185	07RR01P01J185	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J186	07RR01P01J186	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J187	07RR01P01J187	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J188	07RR01P01J188	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J189	07RR01P01J189	SVHS07RR01	NONE
SVHS	v		J1	SVHS07RR01P01	J190	07RR01P01J190	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J191	07RR01P01J191	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J192	07RR01P01J192	SVHS07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		J3	SVHS07RR01P01	J193	07RR01P01J193	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J194	07RR01P01J194	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J195	07RR01P01J195	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J196	07RR01P01J196	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J197	07RR01P01J197	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J198	07RR01P01J198	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J199	07RR01P01J199	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J200	07RR01P01J200	SVHS07RR01	NONE
SVHS	v		J3	SVHS07RR01P01	J201	07RR01P01J201	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J202	07RR01P01J202	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J203	07RR01P01J203	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J204	07RR01P01J204	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J205	07RR01P01J205	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J206	07RR01P01J206	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J207	07RR01P01J207	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J208	07RR01P01J208	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J209	07RR01P01J209	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J210	07RR01P01J210	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J211	07RR01P01J211	SVHS07RR01	NONE
SVHS	v		J5	SVHS07RR01P01	J212	07RR01P01J212	SVHS07RR01	NONE
SVHS	v			SVHS07RR01P01	J213		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J214		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J215		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J216		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J217		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J218		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J219		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J220		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J221		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J222		SVHS07RR01	CAN'T LOCATE
SVHS	v			SVHS07RR01P01	J223		SVHS07RR01	CAN'T LOCATE
SVHS	v		J4	SVHS07RR01P01	J224	07RR01P01J224	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J225	07RR01P01J225	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J226	07RR01P01J226	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J227	07RR01P01J227	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J228	07RR01P01J228	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J229	07RR01P01J229	SVHS07RR01	NONE
SVHS	v		J4	SVHS07RR01P01	J230	07RR01P01J230	SVHS07RR01	NONE
SVHS	v		J2	SVHS07RR01P01	J231	07RR01P01J231	SVHS07RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H1	SVHS03RR01P01	J001	03RR01P01J001	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J002	03RR01P01J002	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J003	03RR01P01J003	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J004	03RR01P01J004	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J005	03RR01P01J005	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J006	03RR01P01J006	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J007	03RR01P01J007	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J008	03RR01P01J008	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J009	03RR01P01J009	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J010	03RR01P01J010	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J011	03RR01P01J011	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J012	03RR01P01J012	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J013	03RR01P01J013	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J014	03RR01P01J014	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J015	03RR01P01J015	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J016	03RR01P01J016	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J017	03RR01P01J017	SVHS03RR01	NONE
SVHS	v		H1	SVHS03RR01P01	J018	03RR01P01J018	SVHS03RR01	NONE
SVHS	v		J2	SVHS03RR01P01	J019	03RR01P01J019	SVHS03RR01	NONE
SVHS	v		J2	SVHS03RR01P01	J020	03RR01P01J020	SVHS03RR01	NONE
SVHS	v		J2	SVHS03RR01P01	J021	03RR01P01J021	SVHS03RR01	NONE
SVHS	v		J2	SVHS03RR01P01	J022	03RR01P01J022	SVHS03RR01	NONE
SVHS	v		J13	SVHS03RR01P01	J023	03RR01P01J023	SVHS03RR01	NONE
SVHS	v		J13	SVHS03RR01P01	J024	03RR01P01J024	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J025	03RR01P01J025	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J026	03RR01P01J026	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J027	03RR01P01J027	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J028	03RR01P01J028	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J029	03RR01P01J029	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J030	03RR01P01J030	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J031	03RR01P01J031	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J032	03RR01P01J032	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J033	03RR01P01J033	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J034	03RR01P01J034	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J035	03RR01P01J035	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J036	03RR01P01J036	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J037	03RR01P01J037	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J038	03RR01P01J038	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J039	03RR01P01J039	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J040	03RR01P01J040	SVHS03RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H2B	SVHS03RR01P01	J041	03RR01P01J041	SVHS03RR01	NONE
SVHS	v		H2B	SVHS03RR01P01	J042	03RR01P01J042	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J043	03RR01P01J043	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J044	03RR01P01J044	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J045	03RR01P01J045	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J046	03RR01P01J046	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J047	03RR01P01J047	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J048	03RR01P01J048	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J049	03RR01P01J049	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J050	03RR01P01J050	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J051	03RR01P01J051	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J052	03RR01P01J052	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J053	03RR01P01J053	SVHS03RR01	NONE
SVHS	v		H3	SVHS03RR01P01	J054	03RR01P01J054	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J055	03RR01P01J055	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J056	03RR01P01J056	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J057	03RR01P01J057	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J058	03RR01P01J058	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J059	03RR01P01J059	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J060	03RR01P01J060	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J061	03RR01P01J061	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J062	03RR01P01J062	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J063	03RR01P01J063	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J064	03RR01P01J064	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J065	03RR01P01J065	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J066	03RR01P01J066	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J067	03RR01P01J067	SVHS03RR01	NONE
SVHS	v		H4	SVHS03RR01P01	J068	03RR01P01J068	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J069	03RR01P01J069	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J070	03RR01P01J070	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J071	03RR01P01J071	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J072	03RR01P01J072	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J073	03RR01P01J073	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J074	03RR01P01J074	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J075	03RR01P01J075	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J076	03RR01P01J076	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J077	03RR01P01J077	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J078	03RR01P01J078	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J079	03RR01P01J079	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J080	03RR01P01J080	SVHS03RR01	NONE



<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H5	SVHS03RR01P01	J081	03RR01P01J081	SVHS03RR01	NONE
SVHS	v		H5	SVHS03RR01P01	J082	03RR01P01J082	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J083	03RR01P01J083	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J084	03RR01P01J084	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J085	03RR01P01J085	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J086	03RR01P01J086	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J087	03RR01P01J087	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J088	03RR01P01J088	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J089	03RR01P01J089	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J090	03RR01P01J090	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J091	03RR01P01J091	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J092	03RR01P01J092	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J093	03RR01P01J093	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J094	03RR01P01J094	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J095	03RR01P01J095	SVHS03RR01	NONE
SVHS	v		H6	SVHS03RR01P01	J096	03RR01P01J096	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J097	03RR01P01J097	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J098	03RR01P01J098	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J099	03RR01P01J099	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J100	03RR01P01J100	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J101	03RR01P01J101	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J102	03RR01P01J102	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J103	03RR01P01J103	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J104	03RR01P01J104	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J105	03RR01P01J105	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J106	03RR01P01J106	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J107	03RR01P01J107	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J108	03RR01P01J108	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J109	03RR01P01J109	SVHS03RR01	NONE
SVHS	v		H7	SVHS03RR01P01	J110	03RR01P01J110	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J111	03RR01P01J111	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J112	03RR01P01J112	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J113	03RR01P01J113	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J114	03RR01P01J114	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J115	03RR01P01J115	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J116	03RR01P01J116	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J117	03RR01P01J117	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J118	03RR01P01J118	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J119	03RR01P01J119	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J120	03RR01P01J120	SVHS03RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H8	SVHS03RR01P01	J121	03RR01P01J121	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J122	03RR01P01J122	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J123	03RR01P01J123	SVHS03RR01	NONE
SVHS	v		H8	SVHS03RR01P01	J124	03RR01P01J124	SVHS03RR01	NONE
SVHS	v			SVHS03RR01P01	J125		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J126		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J127		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J128		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J129		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J130		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J131		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J132		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J133		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J134		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J135		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J136		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J137		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J138		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J139		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J140		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J141		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J142		SVHS03RR01	CAN'T LOCATE
SVHS	v			SVHS03RR01P01	J143		SVHS03RR01	CAN'T LOCATE
SVHS	v		H14	SVHS03RR01P01	J144	03RR01P01J144	SVHS03RR01	NONE
SVHS	v		H14	SVHS03RR01P01	J145	03RR01P01J145	SVHS03RR01	NONE
SVHS	v		H14	SVHS03RR01P01	J146	03RR01P01J146	SVHS03RR01	NONE
SVHS	v		H14	SVHS03RR01P01	J147	03RR01P01J147	SVHS03RR01	NONE
SVHS	v		H14	SVHS03RR01P01	J148	03RR01P01J148	SVHS03RR01	NONE
SVHS	v		H14	SVHS03RR01P01	J149	03RR01P01J149	SVHS03RR01	NONE
SVHS	v		H15	SVHS03RR01P01	J150	03RR01P01J150	SVHS03RR01	NONE
SVHS	v		H15	SVHS03RR01P01	J151	03RR01P01J151	SVHS03RR01	NONE
SVHS	v		H15	SVHS03RR01P01	J152	03RR01P01J152	SVHS03RR01	NONE
SVHS	v		H15	SVHS03RR01P01	J153	03RR01P01J153	SVHS03RR01	NONE
SVHS	v		H15	SVHS03RR01P01	J154	03RR01P01J154	SVHS03RR01	NONE
SVHS	v		H15	SVHS03RR01P01	J155	03RR01P01J155	SVHS03RR01	NONE
SVHS	v		H16	SVHS03RR01P01	J156	03RR01P01J156	SVHS03RR01	NONE
SVHS	v		H16	SVHS03RR01P01	J157	03RR01P01J157	SVHS03RR01	NONE
SVHS	v		H16	SVHS03RR01P01	J158	03RR01P01J158	SVHS03RR01	NONE
SVHS	v		H16	SVHS03RR01P01	J159	03RR01P01J159	SVHS03RR01	NONE
SVHS	v		H16	SVHS03RR01P01	J160	03RR01P01J160	SVHS03RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H16	SVHS03RR01P01	J161	03RR01P01J161	SVHS03RR01	NONE
SVHS	v		H16	SVHS03RR01P01	J162	03RR01P01J162	SVHS03RR01	NONE
SVHS	v		H17	SVHS03RR01P01	J163	03RR01P01J163	SVHS03RR01	NONE
SVHS	v		H17	SVHS03RR01P01	J164	03RR01P01J164	SVHS03RR01	NONE
SVHS	v		H17	SVHS03RR01P01	J165	03RR01P01J165	SVHS03RR01	NONE
SVHS	v		H17	SVHS03RR01P01	J166	03RR01P01J166	SVHS03RR01	NONE
SVHS	v		H17	SVHS03RR01P01	J167	03RR01P01J167	SVHS03RR01	NONE
SVHS	v		H17	SVHS03RR01P01	J168	03RR01P01J168	SVHS03RR01	NONE
SVHS	v		H18	SVHS03RR01P01	J169	03RR01P01J169	SVHS03RR01	NONE
SVHS	v		H18	SVHS03RR01P01	J170	03RR01P01J170	SVHS03RR01	NONE
SVHS	v		H18	SVHS03RR01P01	J171	03RR01P01J171	SVHS03RR01	NONE
SVHS	v		H18	SVHS03RR01P01	J172	03RR01P01J172	SVHS03RR01	NONE
SVHS	v		H18	SVHS03RR01P01	J173	03RR01P01J173	SVHS03RR01	NONE
SVHS	v		H18	SVHS03RR01P01	J174	03RR01P01J174	SVHS03RR01	NONE
SVHS	v		H19	SVHS03RR01P01	J175	03RR01P01J175	SVHS03RR01	NONE
SVHS	v		H19	SVHS03RR01P01	J176	03RR01P01J176	SVHS03RR01	NONE
SVHS	v		H19	SVHS03RR01P01	J177	03RR01P01J177	SVHS03RR01	NONE
SVHS	v		H19	SVHS03RR01P01	J178	03RR01P01J178	SVHS03RR01	NONE
SVHS	v		H19	SVHS03RR01P01	J179	03RR01P01J179	SVHS03RR01	NONE
SVHS	v		H19	SVHS03RR01P01	J180	03RR01P01J180	SVHS03RR01	NONE
SVHS	v		H20	SVHS03RR01P01	J181	03RR01P01J181	SVHS03RR01	NONE
SVHS	v		H20	SVHS03RR01P01	J182	03RR01P01J182	SVHS03RR01	NONE
SVHS	v		H20	SVHS03RR01P01	J183	03RR01P01J183	SVHS03RR01	NONE
SVHS	v		H20	SVHS03RR01P01	J184	03RR01P01J184	SVHS03RR01	NONE
SVHS	v		H20	SVHS03RR01P01	J185	03RR01P01J185	SVHS03RR01	NONE
SVHS	v		H20	SVHS03RR01P01	J186	03RR01P01J186	SVHS03RR01	NONE
SVHS	v		H21	SVHS03RR01P01	J187	03RR01P01J187	SVHS03RR01	NONE
SVHS	v		H21	SVHS03RR01P01	J188	03RR01P01J188	SVHS03RR01	NONE
SVHS	v		H21	SVHS03RR01P01	J189	03RR01P01J189	SVHS03RR01	NONE
SVHS	v		H21	SVHS03RR01P01	J190	03RR01P01J190	SVHS03RR01	NONE
SVHS	v		H22	SVHS03RR01P01	J191	03RR01P01J191	SVHS03RR01	NONE
SVHS	v		H22	SVHS03RR01P01	J192	03RR01P01J192	SVHS03RR01	NONE
SVHS	v		H22	SVHS03RR01P01	J193	03RR01P01J193	SVHS03RR01	NONE
SVHS	v		H22	SVHS03RR01P01	J194	03RR01P01J194	SVHS03RR01	NONE
SVHS	v		H22	SVHS03RR01P01	J195	03RR01P01J195	SVHS03RR01	NONE
SVHS	v		H22	SVHS03RR01P01	J196	03RR01P01J196	SVHS03RR01	NONE
SVHS	v		H23	SVHS03RR01P01	J197	03RR01P01J197	SVHS03RR01	NONE
SVHS	v		H23	SVHS03RR01P01	J198	03RR01P01J198	SVHS03RR01	NONE
SVHS	v		H23	SVHS03RR01P01	J199	03RR01P01J199	SVHS03RR01	NONE
SVHS	v		H23	SVHS03RR01P01	J200	03RR01P01J200	SVHS03RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H23	SVHS03RR01P01	J201	03RR01P01J201	SVHS03RR01	NONE
SVHS	v		H23	SVHS03RR01P01	J202	03RR01P01J202	SVHS03RR01	NONE
SVHS	v		H23	SVHS03RR01P01	J203	03RR01P01J203	SVHS03RR01	NONE
SVHS	v		H 25 OFFICE	SVHS03RR01P01	J204	03RR01P01J204	SVHS03RR01	NONE
SVHS	v		H 25 OFFICE	SVHS03RR01P01	J205	03RR01P01J205	SVHS03RR01	NONE
SVHS	v		H 25 OFFICE	SVHS03RR01P01	J206	03RR01P01J206	SVHS03RR01	NONE
SVHS	v		H 25 OFFICE	SVHS03RR01P01	J207	03RR01P01J207	SVHS03RR01	NONE
SVHS	v		H 25 OFFICE	SVHS03RR01P01	J208	03RR01P01J208	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J209	03RR01P01J209	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J210	03RR01P01J210	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J211	03RR01P01J211	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J212	03RR01P01J212	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J213	03RR01P01J213	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J214	03RR01P01J214	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J215	03RR01P01J215	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J216	03RR01P01J216	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J217	03RR01P01J217	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J218	03RR01P01J218	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J219	03RR01P01J219	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J220	03RR01P01J220	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J221	03RR01P01J221	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J222	03RR01P01J222	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J223	03RR01P01J223	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J224	03RR01P01J224	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J225	03RR01P01J225	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J226	03RR01P01J226	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J227	03RR01P01J227	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J228	03RR01P01J228	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J229	03RR01P01J229	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J230	03RR01P01J230	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J231	03RR01P01J231	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J232	03RR01P01J232	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J233	03RR01P01J233	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J234	03RR01P01J234	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J235	03RR01P01J235	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J236	03RR01P01J236	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J237	03RR01P01J237	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J238	03RR01P01J238	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J239	03RR01P01J239	SVHS03RR01	NONE
SVHS	v		H25	SVHS03RR01P01	J240	03RR01P01J240	SVHS03RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H27	SVHS03RR01P01	J241	03RR01P01J241	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J242	03RR01P01J242	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J243	03RR01P01J243	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J244	03RR01P01J244	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J245	03RR01P01J245	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J246	03RR01P01J246	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J247	03RR01P01J247	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J248	03RR01P01J248	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J249	03RR01P01J249	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J250	03RR01P01J250	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J251	03RR01P01J251	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J252	03RR01P01J252	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J253	03RR01P01J253	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J254	03RR01P01J254	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J255	03RR01P01J255	SVHS03RR01	NONE
SVHS	v		H27	SVHS03RR01P01	J256	03RR01P01J256	SVHS03RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J001	14RR01P01J001	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J002	14RR01P01J002	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J003	14RR01P01J003	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J004	14RR01P01J004	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J005	14RR01P01J005	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J006	14RR01P01J006	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J007	14RR01P01J007	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J008	14RR01P01J008	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J009	14RR01P01J009	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J010	14RR01P01J010	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J011	14RR01P01J011	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J012	14RR01P01J012	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J013	14RR01P01J013	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J014	14RR01P01J014	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J015	14RR01P01J015	SVHS14RR01	NONE
SVHS	v		E1	SVHS14RR01P01	J016	14RR01P01J016	SVHS14RR01	NONE
SVHS	v		E2	SVHS12RR01P01	J043	12RR01P01J015	SVHS12RR01	NONE
SVHS	v		E2	SVHS12RR01P01	J041	12RR01P01J016	SVHS12RR01	NONE
SVHS	v		E3	SVHS12RR01P01	J037	12RR01P01J037	SVHS12RR01	NONE
SVHS	v		E4	SVHS12RR01P01	J036	12RR01P01J036	SVHS12RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J001	02RR01P01J001	SVHS02RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		L1	SVHS02RR01P01	J002	02RR01P01J002	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J003	02RR01P01J003	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J004	02RR01P01J004	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J005	02RR01P01J005	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J006	02RR01P01J006	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J007	02RR01P01J007	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J008	02RR01P01J008	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J009	02RR01P01J009	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J010	02RR01P01J010	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J011	02RR01P01J011	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J012	02RR01P01J012	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J013	02RR01P01J013	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J014	02RR01P01J014	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J015	02RR01P01J015	SVHS02RR01	NONE
SVHS	v		L2	SVHS02RR01P01	J016	02RR01P01J016	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J017	02RR01P01J017	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J018	02RR01P01J018	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J019	02RR01P01J019	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J020	02RR01P01J020	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J021	02RR01P01J021	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J022	02RR01P01J022	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J023	02RR01P01J023	SVHS02RR01	NONE
SVHS	v		L3	SVHS02RR01P01	J024	02RR01P01J024	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J025	02RR01P01J025	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J026	02RR01P01J026	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J027	02RR01P01J027	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J028	02RR01P01J028	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J029	02RR01P01J029	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J030	02RR01P01J030	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J031	02RR01P01J031	SVHS02RR01	NONE
SVHS	v		L4	SVHS02RR01P01	J032	02RR01P01J032	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J033	02RR01P01J033	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J034	02RR01P01J034	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J035	02RR01P01J035	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J036	02RR01P01J036	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J037	02RR01P01J037	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J038	02RR01P01J038	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J039	02RR01P01J039	SVHS02RR01	NONE
SVHS	v		L5	SVHS02RR01P01	J040	02RR01P01J040	SVHS02RR01	NONE
SVHS	v		L6	SVHS02RR01P01	J041	02RR01P01J041	SVHS02RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		L6	SVHS02RR01P01	J042	02RR01P01J042	SVHS02RR01	NONE
SVHS	v		L6	SVHS02RR01P01	J043	02RR01P01J043	SVHS02RR01	NONE
SVHS	v		L6	SVHS02RR01P01	J044	02RR01P01J044	SVHS02RR01	NONE
SVHS	v		L6	SVHS02RR01P01	J045	02RR01P01J045	SVHS02RR01	NONE
SVHS	v		L6	SVHS02RR01P01	J046	02RR01P01J046	SVHS02RR01	NONE
SVHS	v		L6	SVHS02RR01P01	J047	02RR01P01J047	SVHS02RR01	NONE
SVHS	v		L6	SVHS02RR01P01	J048	02RR01P01J048	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J049	02RR01P01J049	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J050	02RR01P01J050	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J051	02RR01P01J051	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J052	02RR01P01J052	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J053	02RR01P01J053	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J054	02RR01P01J054	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J055	02RR01P01J055	SVHS02RR01	NONE
SVHS	v		L7	SVHS02RR01P01	J056	02RR01P01J056	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J057	02RR01P01J057	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J058	02RR01P01J058	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J059	02RR01P01J059	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J060	02RR01P01J060	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J061	02RR01P01J061	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J062	02RR01P01J062	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J063	02RR01P01J063	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J064	02RR01P01J064	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J065	02RR01P01J065	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J066	02RR01P01J066	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J067	02RR01P01J067	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J068	02RR01P01J068	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J069	02RR01P01J069	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J070	02RR01P01J070	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J071	02RR01P01J071	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J072	02RR01P01J072	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J073	02RR01P01J073	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J074	02RR01P01J074	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J075	02RR01P01J075	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J076	02RR01P01J076	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J077	02RR01P01J077	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J078	02RR01P01J078	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J079	02RR01P01J079	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J080	02RR01P01J080	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J081	02RR01P01J081	SVHS02RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		HISTORY	SVHS02RR01P01	J082	02RR01P01J082	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J083	02RR01P01J083	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J084	02RR01P01J084	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J085	02RR01P01J085	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J086	02RR01P01J086	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J087	02RR01P01J087	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J088	02RR01P01J088	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J089	02RR01P01J089	SVHS02RR01	NONE
SVHS	v		HISTORY	SVHS02RR01P01	J090	02RR01P01J090	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J091	02RR01P01J091	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J092	02RR01P01J092	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J093	02RR01P01J093	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J094	02RR01P01J094	SVHS02RR01	NONE
SVHS	v		L1	SVHS02RR01P01	J095	02RR01P01J095	SVHS02RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J001	13RR01P01J001	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J002	13RR01P01J002	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J003	13RR01P01J003	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J004	13RR01P01J004	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J005	13RR01P01J005	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J006	13RR01P01J006	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J007	13RR01P01J007	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J008	13RR01P01J008	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J009	13RR01P01J009	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J010	13RR01P01J010	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J011	13RR01P01J011	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J012	13RR01P01J012	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J013	13RR01P01J013	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J014	13RR01P01J014	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J015	13RR01P01J015	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J016	13RR01P01J016	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J017	13RR01P01J017	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J018	13RR01P01J018	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J019	13RR01P01J019	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J020	13RR01P01J020	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J021	13RR01P01J021	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J022	13RR01P01J022	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J023	13RR01P01J023	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J024	13RR01P01J024	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J025	13RR01P01J025	SVHS13RR01	NONE



<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J026	13RR01P01J026	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J027	13RR01P01J027	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J028	13RR01P01J028	SVHS13RR01	NONE
SVHS	v		H1/COMP LAB	SVHS13RR01P01	J029	13RR01P01J029	SVHS13RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J001	06RR01P01J001	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J002		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J003		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J004	06RR01P01J004	SVHS06RR01	CAN'T LOCATE
SVHS	v		M3	SVHS06RR01P01	J005	06RR01P01J005	SVHS06RR01	
SVHS	v		M3	SVHS06RR01P01	J006	06RR01P01J006	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J007		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J008		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J009		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J010		SVHS06RR01	CAN'T LOCATE
SVHS	v		M1	SVHS06RR01P01	J011	06RR01P01J011	SVHS06RR01	NONE
SVHS	v		M1	SVHS06RR01P01	J012	06RR01P01J012	SVHS06RR01	NONE
SVHS	v		M1	SVHS06RR01P01	J013	06RR01P01J013	SVHS06RR01	NONE
SVHS	v		M1	SVHS06RR01P01	J014	06RR01P01J014	SVHS06RR01	NONE
SVHS	v		M1	SVHS06RR01P01	J015	06RR01P01J015	SVHS06RR01	NONE
SVHS	v		M1	SVHS06RR01P01	J016	06RR01P01J016	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J017	06RR01P01J017	SVHS06RR01	NONE
SVHS	v		M1	SVHS06RR01P01	J018	06RR01P01J018	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J019	06RR01P01J019	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J020	06RR01P01J020	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J021	06RR01P01J021	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J022	06RR01P01J022	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J023	06RR01P01J023	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J024	06RR01P01J024	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J025	06RR01P01J025	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J026	06RR01P01J026	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J027	06RR01P01J027	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J028	06RR01P01J028	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J029	06RR01P01J029	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J030	06RR01P01J030	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J031	06RR01P01J031	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J032	06RR01P01J032	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J033	06RR01P01J033	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J034	06RR01P01J034	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J035		SVHS06RR01	CAN'T LOCATE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v			SVHS06RR01P01	J036		SVHS06RR01	CAN'T LOCATE
SVHS	v		M7	SVHS06RR01P01	J037	06RR01P01J037	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J038	06RR01P01J038	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J039	06RR01P01J039	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J040	06RR01P01J040	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J041	06RR01P01J041	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J042		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J043		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J044		SVHS06RR01	CAN'T LOCATE
SVHS	v		M5	SVHS06RR01P01	J045	06RR01P01J045	SVHS06RR01	CAN'T LOCATE
SVHS	v		M5	SVHS06RR01P01	J046	06RR01P01J046	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J047		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J048		SVHS06RR01	CAN'T LOCATE
SVHS	v		SCI. LAB	SVHS06RR01P01	J049	06RR01P01J049	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J050	06RR01P01J050	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J051	06RR01P01J051	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J052	06RR01P01J052	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J053	06RR01P01J053	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J054	06RR01P01J054	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J055	06RR01P01J055	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J056	06RR01P01J056	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J057	06RR01P01J057	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J058	06RR01P01J058	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J059	06RR01P01J059	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J060	06RR01P01J060	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J061	06RR01P01J061	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J062	06RR01P01J062	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J063	06RR01P01J063	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J064	06RR01P01J064	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J065	06RR01P01J065	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J066	06RR01P01J066	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J067	06RR01P01J067	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J068	06RR01P01J068	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J069	06RR01P01J069	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J070	06RR01P01J070	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J071	06RR01P01J071	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J072	06RR01P01J072	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J073	06RR01P01J073	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J074	06RR01P01J074	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J075	06RR01P01J075	SVHS06RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		SCI. LAB	SVHS06RR01P01	J076	06RR01P01J076	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J077	06RR01P01J077	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J078	06RR01P01J078	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J079	06RR01P01J079	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J080	06RR01P01J080	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J081	06RR01P01J081	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J082	06RR01P01J082	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J083	06RR01P01J083	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J084	06RR01P01J084	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J085	06RR01P01J085	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J086	06RR01P01J086	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J087	06RR01P01J087	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J088	06RR01P01J088	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J089	06RR01P01J089	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J090	06RR01P01J090	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J091	06RR01P01J091	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J092	06RR01P01J092	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J093	06RR01P01J093	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J094	06RR01P01J094	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J095	06RR01P01J095	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J096	06RR01P01J096	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J097	06RR01P01J097	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J098	06RR01P01J098	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J099	06RR01P01J099	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J100	06RR01P01J100	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J101	06RR01P01J101	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J102	06RR01P01J102	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J103	06RR01P01J103	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J104	06RR01P01J104	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J105		SVHS06RR01	CAN'T LOCATE
SVHS	v		M1	SVHS06RR01P01	J106	06RR01P01J106	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J107		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J108		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J109		SVHS06RR01	CAN'T LOCATE
SVHS	v		M1	SVHS06RR01P01	J110	06RR01P01J110	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J111	06RR01P01J111	SVHS06RR01	NONE
SVHS	v		M1	SVHS06RR01P01	J112	06RR01P01J112	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J113	06RR01P01J113	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J114	06RR01P01J114	SVHS06RR01	NONE
SVHS	v		M2	SVHS06RR01P01	J115	06RR01P01J115	SVHS06RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		M2	SVHS06RR01P01	J116	06RR01P01J116	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J117	06RR01P01J117	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J118	06RR01P01J118	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J119	06RR01P01J119	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J120	06RR01P01J120	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J121	06RR01P01J121	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J122	06RR01P01J122	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J123	06RR01P01J123	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J124	06RR01P01J124	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J125	06RR01P01J125	SVHS06RR01	NONE
SVHS	v		M4	SVHS06RR01P01	J126	06RR01P01J126	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J127	06RR01P01J127	SVHS06RR01	NONE
SVHS	v		SCI. LAB	SVHS06RR01P01	J128	06RR01P01J128	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J129	06RR01P01J129	SVHS06RR01	NONE
SVHS	v		M7	SVHS06RR01P01	J130	06RR01P01J130	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J131		SVHS06RR01	CAN'T LOCATE
SVHS	v		M5	SVHS06RR01P01	J132	06RR01P01J132	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J133	06RR01P01J133	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J134	06RR01P01J134	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J135	06RR01P01J135	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J136	06RR01P01J136	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J137	06RR01P01J137	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J138	06RR01P01J138	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J139	06RR01P01J139	SVHS06RR01	NONE
SVHS	v		M5	SVHS06RR01P01	J140	06RR01P01J140	SVHS06RR01	NONE
SVHS	v			SVHS06RR01P01	J141		SVHS06RR01	CAN'T LOCATE
SVHS	v			SVHS06RR01P01	J142		SVHS06RR01	CAN'T LOCATE
SVHS	v		M3	SVHS06RR01P01	J143	06RR01P01J143	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J144	06RR01P01J144	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J145	06RR01P01J145	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J146	06RR01P01J146	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J147	06RR01P01J147	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J148	06RR01P01J148	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J149	06RR01P01J149	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J150	06RR01P01J150	SVHS06RR01	NONE
SVHS	v		M3	SVHS06RR01P01	J151	06RR01P01J151	SVHS06RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J001	09RR01P01J001	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J002	09RR01P01J002	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J003	09RR01P01J003	SVHS09RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		PAVILLION	SVHS09RR01P01	J004	09RR01P01J004	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J005	09RR01P01J005	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J006	09RR01P01J006	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J007	09RR01P01J007	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J008	09RR01P01J008	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J009	09RR01P01J009	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J010	09RR01P01J010	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J011	09RR01P01J011	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J012	09RR01P01J012	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J013	09RR01P01J013	SVHS09RR01	NONE
SVHS	v		PAVILLION	SVHS09RR01P01	J014	09RR01P01J014	SVHS09RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J001	10RR01P01J001	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J002	10RR01P01J002	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J003	10RR01P01J003	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J004	10RR01P01J004	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J005	10RR01P01J005	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J006	10RR01P01J006	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J007		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J008		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J009	10RR01P01J009	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J010	10RR01P01J010	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J011		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J012		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J013		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J014		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J015	10RR01P01J015	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J016	10RR01P01J016	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J017		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J018		SVHS10RR01	CAN'T LOCATE
SVHS	v		S.C.O.E	SVHS10RR01P01	J019	10RR01P01J019	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J020	10RR01P01J020	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J021	10RR01P01J021	SVHS10RR01	NONE
SVHS	v		S.C.O.E	SVHS10RR01P01	J022	10RR01P01J022	SVHS10RR01	NONE
SVHS	v		G1	SVHS05RR01P01	J001	05RR01P01J001	SVHS05RR01	NONE
SVHS	v		G3	SVHS05RR01P01	J002	05RR01P01J002	SVHS05RR01	NONE
SVHS	v		G5	SVHS05RR01P01	J003	05RR01P01J003	SVHS05RR01	NONE
SVHS	v		G7	SVHS05RR01P01	J004	05RR01P01J004	SVHS05RR01	NONE
SVHS	v		G2	SVHS05RR01P01	J005	05RR01P01J005	SVHS05RR01	NONE

<u>Site Name</u>	<u>Cat5</u>	<u>Cat 5E</u>	<u>Room#</u>	<u>Patch Panel</u>	<u>Port Number (Patch panel side)</u>	<u>Port Number (Station side)</u>	<u>MDF/IDF</u>	<u>Comments</u>
SVHS	v		G4	SVHS05RR01P01	J006	05RR01P01J006	SVHS05RR01	NONE
SVHS	v		G6	SVHS05RR01P01	J007	05RR01P01J007	SVHS05RR01	NONE
SVHS	v		G8	SVHS05RR01P01	J008	05RR01P01J008	SVHS05RR01	NONE
SVHS	v		G8	SVHS05RR01P01	J009	05RR01P01J009	SVHS05RR01	NONE
SVHS	v		G6	SVHS05RR01P01	J010	05RR01P01J010	SVHS05RR01	NONE
SVHS	v		G4	SVHS05RR01P01	J011	05RR01P01J011	SVHS05RR01	NONE
SVHS	v		G2	SVHS05RR01P01	J012	05RR01P01J012	SVHS05RR01	NONE
SVHS	v		G1	SVHS05RR01P01	J013	05RR01P01J013	SVHS05RR01	NONE
SVHS	v		G3	SVHS05RR01P01	J014	05RR01P01J014	SVHS05RR01	NONE
SVHS	v		G5	SVHS05RR01P01	J015	05RR01P01J015	SVHS05RR01	NONE
SVHS	v		G7	SVHS05RR01P01	J016	05RR01P01J016	SVHS05RR01	NONE

FISCAL CRISIS & MANAGEMENT ASSISTANCE TEAM  
STUDY AGREEMENT  
July 30, 2008

The FISCAL CRISIS AND MANAGEMENT ASSISTANCE TEAM (FCMAT), hereinafter referred to as the Team, and the Sonoma Valley Unified School District hereinafter referred to as the District, mutually agree as follows:

1. BASIS OF AGREEMENT

The Team provides a variety of services to school districts and county offices of education upon request. The District has requested that the Team provide for the assignment of professionals to study specific aspects of the Sonoma Valley Unified School District operations. These professionals may include staff of the Team, County Offices of Education, the California State Department of Education, school districts, or private contractors. All work shall be performed in accordance with the terms and conditions of this Agreement.

2. SCOPE OF THE WORK

A. Scope and Objectives of the Study

The scope and objectives of this study are to:

- 1) Conduct a review of the district's administrative technology and make recommendations for improvement.
- 2) Conduct a review of the district's instructional technology and make recommendations for improvement.
- 3) Conduct a review of the district's technology services delivery and make recommendations for improvement.

B. Services and Products to be Provided

- 1) Orientation Meeting - The Team will conduct an orientation session at the District to brief District management and supervisory personnel on the procedures of the Team and on the purpose and schedule of the study.

- 2) On-site Review - The Team will conduct an on-site review at the District office and at school sites if necessary.
- 3) Progress Reports - The Team will hold an exit meeting at the conclusion of the on-site review to inform the District of significant findings and recommendations to that point.
- 4) Exit Letter - The Team will issue an exit letter approximately 10 days after the exit meeting detailing significant findings and recommendations to date and memorializing the topics discussed in the exit meeting.
- 5) Draft Reports - Sufficient copies of a preliminary draft report will be delivered to the District administration for review and comment.
- 6) Final Report - Sufficient copies of the final study report will be delivered to the District following completion of the review.
- 7) Follow-Up Support – Six months after the completion of the study, FCMAT will return to the District, if requested, to confirm the District’s progress in implementing the recommendations included in the report, at no costs. Status of the recommendations will be documented to the District in a FCMAT Management Letter.

3. PROJECT PERSONNEL

The study team will be supervised by Anthony L. Bridges, Deputy Executive Officer, Fiscal Crisis and Management Assistance Team, Kern County Superintendent of Schools Office. The study team may also include:

- A. Andrew Prestage, FCMAT Management Analyst
- B. Steven Carr, FCMAT Technology Consultant
- C. Terrell Tucker, FCMAT Technology Consultant
- D. Bradley White, FCMAT Technology Consultant

Other equally qualified consultants will be substituted in the event one of the above noted individuals is unable to participate in the study.

4. PROJECT COSTS

The cost for studies requested pursuant to E.C. 42127.8(d)(1) shall be:

- A. \$500.00 per day for each Team Member while on site, conducting fieldwork at other locations, preparing and presenting reports, or participating in meetings.



B. All out-of-pocket expenses, including travel, meals, lodging, etc. The District will be billed for the daily rate and expenses of the independent consultant, only. Based on the elements noted in section 2 A, the total cost of the study is estimated at \$18,000.00. The District will be invoiced at actual costs, with 50% of the estimated cost due following the completion of the on-site review and the remaining amount due upon acceptance of the final report by the District

C. Any change to the scope will affect the estimate of total cost.

Payments for FCMAT services are payable to Kern County Superintendent of Schools-Administrative Agent.

5. RESPONSIBILITIES OF THE DISTRICT

A. The District will provide office and conference room space while on-site reviews are in progress.

B. The District will provide the following (if requested):

- 1) A map of the local area
- 2) Existing policies, regulations and prior reports addressing the study request
- 3) Current organizational charts
- 4) Current and four (4) prior year's audit reports
- 5) Any documents requested on a supplemental listing

C. The District Administration will review a preliminary draft copy of the study. Any comments regarding the accuracy of the data presented in the report or the practicability of the recommendations will be reviewed with the Team prior to completion of the final report.

Pursuant to EC 45125.1(c), representatives of FCMAT will have limited contact with District pupils. The District shall take appropriate steps to comply with EC 45125.1(c).

6. PROJECT SCHEDULE

The following schedule outlines the planned completion dates for key study milestones:

Orientation:	September 4, 2008
Staff Interviews:	to be determined
Exit Interviews:	September 5, 2008
Preliminary Report Submitted:	to be determined
Final Report Submitted:	to be determined
Board Presentation:	to be determined
Follow-Up Support:	If requested

7. CONTACT PERSON

Please print name of contact person: Justin Frese, Associate Superintendent

Telephone 707-939-2237 FAX \_\_\_\_\_

Internet Address www.sonomavly.org

 \_\_\_\_\_ 8.25.08

Justin Frese, Assistant Superintendent, Business Date  
Sonoma Valley Unified School District

 \_\_\_\_\_ July 30, 2008

Barbara Dean, Deputy Administrative Officer Date  
Fiscal Crisis and Management Assistance Team

In keeping with the provisions of AB1200, the County Superintendent will be notified of this agreement between the District and FCMAT and will receive a copy of the final report.